

Impact of Hole Transport Layers in Inorganic Lead-Free B- γ -CsSnI₃ Perovskite Solar Cells: A Numerical Analysis [†]

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Abstract: Tin-based halide perovskite compounds have attracted enormous interest as effective replacement to the conventional lead halide perovskite solar cells (PSCs). However, getting high efficiency for tin-based perovskite solar cells is still challenging. Herein, we introduce copper sulfide (CuS) as hole transport material (HTM) in lead free tin-based B- γ -CsSnI₃ PSCs to enhance the photovoltaic (PV) performances. The lead free tin-based CsSnI₃ perovskite solar cell structure consisting of CuS/CsSnI₃/TiO₂/ITO has been modeled and investigated the output characteristics by using the one dimensional solar cell capacitance simulator (SCAPS-1D). The CuS hole transport layer (HTL) with proper band arrangement may notably minimize the recombination of charge carrier at the back side of the perovskite absorber. Density functional theory (DFT) -extracted physical parameters including band gap and absorption spectrum of CuS are used in the SCAPS-1D program to analyze the characteristics of the proposed PV device. The PV performance parameters of the proposed device are numerically evaluated by varying the absorber thickness and doping concentration. In this work, the variation of the functional temperature on the cell outputs is also studied. Furthermore, different HTMs are employed to investigate the PV characteristics of the proposed CsSnI₃ PSC. The power conversion efficiency (PCE) of ~29% is achieved with open circuit voltage (V_{oc}) of 0.99 V, fill factor of ~87%, and short circuit current density (J_{sc}) of 33.5 mA/cm² for the optimized device. This work addresses a proper guideline and introduces a convenient approach to design and fabricate highly efficient, inexpensive, and stable lead free tin-based perovskite solar cells.

Keywords: perovskite; B- γ -CsSnI₃; HTL; CuS; DFT; SCAPS-1D

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1. Introduction

The PSCs have fascinated great consideration as encouraging PV technology due to admirable properties associated with excellent PCE and low fabrication cost. This new class of PV technology has recently received enormous interest owing to the emerging conversion efficiency of ~25% [1,2]. However, the rapid growth and commercialization of PSCs are impeded because of toxicity present in most commonly developed lead-based perovskite solar cells [3]. In this context, various attempts have been made in search of suitable alternative for the lead-based perovskites [4–6]. Among different perovskite materials, the inorganic cesium tin triiodide (CsSnI₃) may be considered as one of the potential candidates [7]. CsSnI₃ exhibits suitable optoelectronic properties including an ideal range of energy gap of ~1.3 eV, absorption coefficient (10⁴ cm⁻¹), high charge-carrier mobilities (above 500 cm² V⁻¹ s⁻¹) and low exciton binding energy (~18 meV) [7,8]. In the previous work, an efficiency of 0.9% is reported with the architecture of indium tin oxide/CsSnI₃/Au/Ti in 2012 [9]. An earlier research evaluated a maximum power conversion

efficiency (PCE) of 5.03% for CsSnI₃ PVQD-based solar cells [10]. Recently an enhanced efficiency of 6.08% has been measured experimentally by using mixed electron transport layer with CsSnI₃ absorber [11]. Another recent work shows a highest experimental conversion efficiency of 7.50% for the configuration of CsSnI₃ absorber with poly (3-hexylthiophene) as HTL [12]. Very recently, a numerical work on the TiO₂/CsSnI₃/Spiro-OMeTAD PSC has evaluated a power conversion efficiency of 20.2% with V_{oc} of 0.97 V [13]. However, these efficiencies are still lower than the other available lead-based PSCs. For a PSC to operate efficiently, the HTL material is a pivotal factor. Insertion of HTL can speed up the hole extraction along the minimization of carrier recombination by blocking the electron flow [14]. In this work, for the first time, we have introduced copper sulfide (CuS) as hole transport material with the CsSnI₃ absorber with TiO₂ as ETL. CuS is p-type semiconducting material which has previously been utilized as HTL with other inorganic solar cells [15]. Suitable semiconducting properties of CuS attained from DFT-extraction in the present work [16–18], provide proper band alignment with CsSnI₃ to reduce the carrier recombination and enhance the PV performance of the cell.

This work represents the simulation and performance analysis of the PSC with the novel architecture of CuS/CsSnI₃/TiO₂/ITO by utilizing the SCAPS-1D program. To achieve the optimized performance, the output parameters are evaluated by varying the thickness, doping concentration, and bulk defect density of the absorber.

2. Methods and Materials

This present work has performed first principle DFT utilizing Cambridge Serial Total Energy Package (CASTEP) open-source package [19] on CuS HTL to evaluate the optical and electrical properties and designed a novel heterojunction lead-free CsSnI₃ PSC with arrangement of CuS/CsSnI₃/TiO₂/ITO. The designed PSC is investigated numerically using the SCAPS-1D which is solved the Poisson’s and continuity equation intending to evaluate one and two-dimension semiconductor cells [20]. In the present study, CuS and TiO₂ are utilized as hole transport layer (HTL) at the back of the absorber and electron transport layer (ETL), respectively. Indium tin oxide (ITO) as transparent conducting oxide (TCO) is used in this numerical investigation. Aluminum (Al) having work function of 4.06 eV [21] and Nickel (Ni) having work function of 5.15 eV [21] are used as metallic electrodes at the front contact and back contact, respectively. Table 1 illustrates the electrical and optical parameters used in this numerical investigation to evaluate the output characteristics of the proposed device. The parameters of all the layers are well agreed with some previous experimental and theoretical works [22–31].

Table 1. Parameters used in this study to investigate the device numerically.

Parameters	CuSCN	CuI	NiO _x	MoO ₃	CuS	CsSnI ₃	TiO ₂	ITO
Bandgap, E _g (eV)	3.6	3.1	3.8	3	1.55	1.3	3.2	3.5
Electron affinity, χ (eV)	1.7	2.1	1.46	2.5	3.89	3.8	4.1	4.6
Electron/hole mobility (cm ² V ⁻¹ s ⁻¹)	100/25	100/43.9	12/2.8	25/100	12/9	50/400	0.006	10/10
Thickness (μm)	0.1	0.1	0.1	0.1	0.1	0.6	0.05	0.05
Carrier concentration (cm ⁻³)	1 × 10 ¹⁸	1 × 10 ¹⁸	1 × 10 ¹⁸	1 × 10 ¹⁸	4.7 × 10 ¹⁸	5 × 10 ¹⁷	1 × 10 ¹⁸	1 × 10 ²¹
Reference	[22]	[22]	[23]	[24]	[25]	[26–28]	[29,30]	[31]

3. Result and Discussion

3.1. Structural Properties and Band Structure of CuS

In this study, CASTEP toolkit package [19] is engaged to accomplish the geometry optimization, Elastic constants, band structure, density of states, and optical properties computation. The cut-off kinetic-energy of plane wave is 400 eV with grid parameters (a b c) of 10 10 2 having actual spacing 0.014296 (1/Å) 0.014296 (1/Å) 0.014327 (1/Å). The lowest deviation of 27.1% is calculated using the initial cell volume and final optimized cell volume at cutoff energy 400 eV. The generalized gradient approximation (GGA) for the exchange correlation energy is taken, instead of local density approximation (LDA), due to its excellent band structure calculation. The lattice parameters of CuS are $a = b = 3.797 \text{ \AA}$, $c = 16.441 \text{ \AA}$, $\alpha = \beta = 90^\circ$, $\gamma = 120^\circ$ [32]. The geometry optimized crystal structure of unit cell is depicted in Figure 1a. Figure 1b shows the band structure for the optimized CuS structure which is in well concurrence with the density of state. Band structure calculation also shows the band-gap of $\sim 1.6 \text{ eV}$. The band-gap analysis of CuS via DFT calculations with hexagonal structure is almost in the concurrence with the band-gap of the hole transport layer (CuS $\sim 1.5 \text{ eV}$).

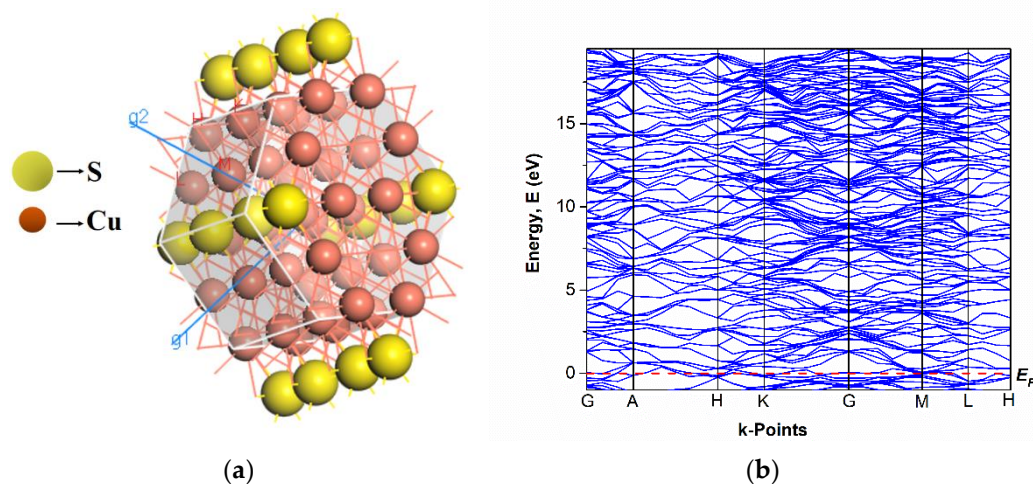


Figure 1. (a) Crystal structure; (b) band structure of CuS.

3.2. Impact of HTLs on Device Performances

To perceive the characteristics of the designed CsSnI₃ PSC, numerous HTLs are investigated in this study and the resulted current density-voltage (J-V) characteristics are revealed in Figure 2. This work introduces several HTLs including CuSCN, CuI, CuS, NiOx, and MoO₃ at the back of the CsSnI₃ perovskite absorber to improve the proposed device performances by minimizing the losses of charge carrier recombination at the back of the absorber. It is noticed from Figure 2 that lead-free tin based CsSnI₃ PSC performances such as open-circuit voltage (V_{oc}), short circuit current density (J_{sc}), power conversion efficiency (PCE), and fill factor (FF) are improved with inserting the defined HTLs. By inserting HTLs, strong built-in electric field is created at the back of the absorber, thus results in improvement of V_{oc} of the proposed device. Consequently, J_{sc} is also enhanced as ease conduction of charge carrier from absorber to back metal contact and the collection of charge carrier by the electrode. Additionally, introducing HTLs at the back of the absorber may reduce the recombination losses at the back interface and increase the performances of the device. Furthermore, it is important to select the proper HTL among the defined HTLs. The lower negative valance band offset (VBO) is needed to accumulate the holes from the absorber and create the proper band alignment, thus increasing the cell efficiency. Among the different HTLs, CuS has lower VBO than others and gives higher cell outputs which are also illustrated in Figure 2 (inset). Therefore, CuS as HTL is introduced in lead-free CsSnI₃ PSC.

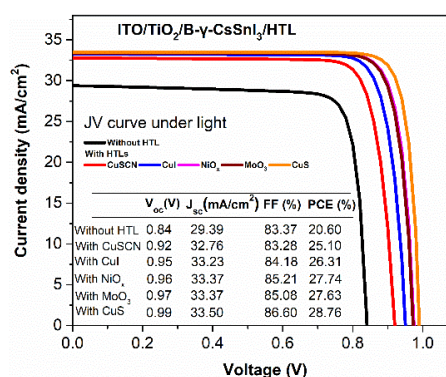


Figure 2. J-V characteristics and solar outputs with different HTLs.

3.3. Effect of Absorber Thickness on Cell Performances

The photon energy is absorbed by the absorber layer. In order to realize the device characteristics, thickness of the CsSnI₃ perovskite absorber layer is shifted from 0.1 μm to 1.4 μm as presented in Figure 3a. Thickness of 0.1 μm, 0.05 μm, and 0.05 μm for CuS HTL, TiO₂ ETL, and ITO, respectively, are fixed during the analysis of absorber layer thickness. It is revealed from the figure that V_{oc} is decreased with increasing the absorber layer thickness till 1.2 μm and then it is almost saturated. The degradation of V_{oc} with absorber thickness can be described as the increment of series resistance and recombination rate. On the contrary, J_{sc} is linearly increased upto the absorber thickness of 0.4 μm as the improved generated charge carriers and a comparatively small increment of J_{sc} is observed beyond the thickness of 0.5 μm. The absorption of light is significantly lower when the thickness of the absorber layer is lower, therefore thicker absorber is needed to absorb the sufficient light for enhancing the current density and efficiency. The variation of fill factor (FF) did not changed notably throughout the entire thickness of the absorber. In addition, the PCE is improved upto the absorber thickness of 0.6 μm. Beyond 0.6-μm-thick absorber, less significant enhancement of the PCE is observed. Therefore, absorber thickness is optimized to be 0.6 μm. At the thickness of 0.6 μm, PCE is recorded to be 28.76% including V_{oc} of 0.99 V, J_{sc} of 33.5 mA/cm², and FF of 86.6% in the present study.

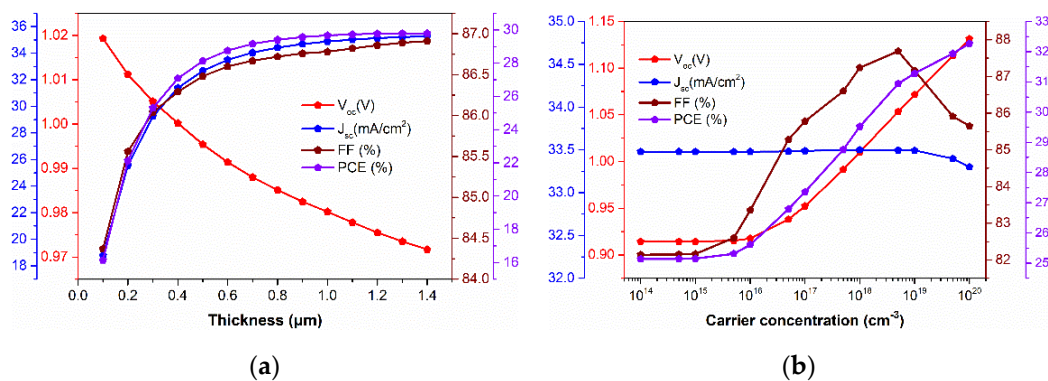


Figure 3. Characteristics of CsSnI₃ perovskite solar cell with (a) absorber layer thickness; (b) carrier concentration.

3.4. Effect of Carrier Concentration on Cell Performances

To perceive the output characteristics of the proposed device in accordance with carrier concentration of the absorber, the range from 10¹⁴ cm⁻³ to 10²⁰ cm⁻³ of carrier concentration of the absorber is examined. Figure 3b represents the variation of carrier concentration of the absorber. During the investigation of doping density of absorber, doping density of 10¹⁹ cm⁻³, 10¹⁸ cm⁻³, and 10²¹ cm⁻³ for CuS HTL, TiO₂ ETL, and ITO, respectively, are fixed. It is noticed from Figure 3b that, device output parameters such as V_{oc} , FF, and

PCE are incremented with boosting the doping density of the absorber. Improvement of carrier concentration in the absorber layer may create enough charge carriers, thereby enhancing the PV performances of the perovskite solar device. The value of PCE > 28.76% and V_{oc} > 0.99 V are measured when the doping density is larger than $5 \times 10^{17} \text{ cm}^{-3}$. Conversely, J_{sc} is declined with boosting the absorber carrier concentration as the increment of augur recombination and resistivity of the absorber. At doping density of $5 \times 10^{17} \text{ cm}^{-3}$, J_{sc} of 33.5 mA/cm² is estimated. This works optimized the doping density of $5 \times 10^{17} \text{ cm}^{-3}$ considering the overall performances and this optimized value has consistency with the previous experimental works [27].

3.5. Effect of Temperature on Cell Performances

The characteristics and output parameters of solar cells are immensely dependent on temperature. The reduction in energy bandgap of the material allows spare absorption of the photons of small energy which leads to increase the short-circuit current. On the other hand, reduction in the bandgap results in diminishing the open-circuit voltage [33,34]. In the interest of realizing the stability of PSCs, the influence of temperature on device performances is shifted from 283 K to 483 K as depicted in Figure 4. As is noticeable from the figure that, the device output parameters including V_{oc} , PCE, and FF are declined with escalating the conducting temperature. V_{oc} of 1.02 V, PCE of 29.79%, and FF of 88.01% are determined at temperature of 273 K and these values are reduced to 0.75 V, 77.2%, and 19.36% for V_{oc} , PCE, and FF, respectively, at temperature 473 K. The value of J_{sc} is rarely increased with temperature as almost constant as the reverse-saturation current density ameliorates with temperature [34].

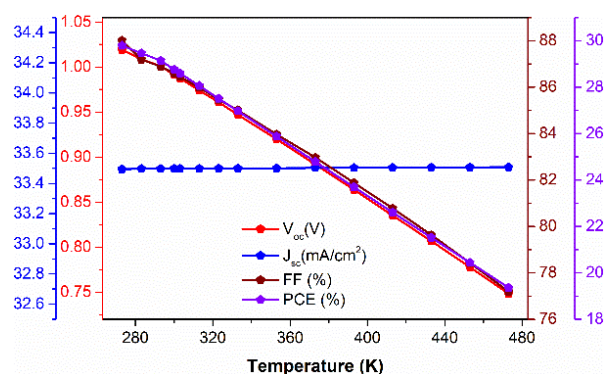


Figure 4. Influence of temperature on the designed perovskite solar cell.

4. Conclusions

This work designs lead-free tin based B- γ -CsSnI₃ PSC with the arrangement of CuS/CsSnI₃/TiO₂/ITO where the CuS is utilized as HTL, TiO₂ as ETL. Initially, some optical and electrical properties are analyzed for CuS HTL using the first principle density functional theory (DFT). After that, the characteristics of the proposed device structure are assessed numerically by using the SCAPS-1D. The PV performances are evaluated by varying the thickness, carrier concentration of the absorber layer. Herein, numerous HTLs have been realized to adopt the worthy HTL for B- γ -CsSnI₃ PSC. The best power conversion efficiency (PCE) of 28.76% containing V_{oc} of 0.99 V, J_{sc} of 33.5 mA/cm², and FF of 86.6% has been attained with the optimized thickness of 0.1 μm , 0.6 μm , 0.05 μm , and 0.05 μm for the CuS HTL, absorber, TiO₂ ETL, and ITO, respectively. After providing the overall examination on B- γ -CsSnI₃ PSCs, this work has recommend that CuS can be utilized as prominent HTL at the back of the absorber to design and fabricate earth abundant and cost-effective B- γ -CsSnI₃ PSC.

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