Improved Virtual Synchronous Generator Based Control Scheme for Enhanced Transient Response in Microgrids †

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Abstract: Synchronous generator-based power stations with their inherent inertia, can maintain frequency stability during sudden load switching. While distributed generating station-driven microgrids suffer from a lack of natural inertia. Cascaded power, voltage, and current controllers are a widespread control strategy used to regulate the power output of distributed generating stations to maintain frequency and voltage within stable limits. Virtual synchronous generator (VSG) control for the power controller is used as a potential solution to emulate inertia. To derive maximum benefit from VSG, proper tuning of its multiple parameters is required. In this direction, earlier works proposed the equivalence between the droop and VSG schemes which suggested that the droop coefficient value can be directly used in the design of VSG. As an improvement to these conventional works, the proposed work in this paper identifies that VSG delivers a better response when an equalizing constant is used to adjust the droop coefficient value than using it directly. This paper proposes implementing the VSG with an equalizing constant as a new design parameter. A description of designing the parameters of this improved VSG considering the equalizing constant is also discussed in this paper. The performance of the conventional VSG and the proposed improved VSG are compared. From the results, it is observed that, at load switching, the output frequency of the proposed method in all test cases has settled in less than 3 s while the conventional method took a maximum of 6 s in critical cases. Further, the output frequency’s maximum peak with the proposed method is 3 Hz less than the conventional method. These, along with other metrics, validate the importance of the proposed improved VSG-based control scheme for the enhancement of transient response in microgrids.

Keywords: frequency stability; microgrids; transient response; virtual synchronous generator

1. Introduction

To provide uninterrupted and quality electricity, distributed generating sources are located near local loads. By interconnecting smaller capacity distributed generation stations (DGs) together, the concept of microgrids has emerged [1]. Most DGs use power electronics converters to connect the source and load. These power electronics-driven DGs due to their smaller capacity cannot help in the improvement of transient response from inherent inertia [2]. This led to the development of virtual inertia techniques using power controllers. Four varieties of virtual inertia emulation schemes are available in the literature. They are (a) droop control [3], (ii) Synchronverter [4], (iii) current-controlled virtual synchronous generator (VSG) [5], and (iv) voltage-controlled VSG [6]. Among these, voltage-controlled VSG exhibits better transient response in both island and grid-connected modes. A typical layout of the microgrid with a VSG-based power controller is shown in Figure 1. These VSG controllers by emulating virtual inertia improve frequency stability in both transient and steady-state conditions. Even though the stability of
frequency is guaranteed during steady-state, the response during the transient stage may take a longer time to settle. This may lead to unnecessary tripping caused by frequency relay with its definite time delay setting. Therefore, a study to modify the conventional VSG control to reduce the chances of unnecessary tripping in microgrids is essential.

![Figure 1. Typical layout of single DGS-based microgrid with VSG-based multiloop control scheme.](image)

To analyze the issue, studying the impact of VSG parameters on stability and response is crucial. Stability analysis of voltage-controlled VSG through small signal modeling considering parameter changes is covered in [7]. Literature suggests that adjusting the VSG’s inertia coefficient enhances transient response, with [8] presenting newer methods for this. However, these bring longer computation times and insufficient results [9].

Through the comparison between active power loops of droop control and VSG control, ref. [10] identifies the relation between the speed governor proportional gain constant ($K_p\omega$) as the inverse of active power–frequency ($P_\omega$) droop coefficient ($D_p$). In [9], adjusting $K_p\omega$ in the VSG setup is recommended for enhanced performance, but without a specified process. This study reveals that using an equalizing factor to modify $K_p\omega$ reduces unwanted tripping during significant load shifts. A procedure for designing this improved VSG (MVSG) control using the equalizing factor is detailed here. Simulations under diverse load conditions validate the effectiveness of the proposed approach in improving transient performance.

The remaining sections of the paper are organized as follows. Section 2 describes the conventional VSG. Section 3 presents the proposed MVSG. Section 4 analyses the results obtained by simulating the proposed scheme at various load conditions. Finally, the conclusions of the proposed work are discussed in Section 5.

2. Conventional Virtual Synchronous Generator

A VSG will emulate virtual inertia that is similar to the inertia of traditional synchronous generators by making use of electrical energy storage systems. This improves stability under transient conditions. The swing equation of the conventional synchronous generator (1) which is translated into an algorithm is used for developing an active power loop in the VSG. Where $P^*$ is the equivalent of reference mechanical power input, $P_{load}$ is the electrical power output, $J$ is equivalent of rotor inertia, $D$ is the damping constant, $\omega_n$ is the nominal angular frequency which normally is equivalent to 50 Hz and $\omega_{vsg}$ is the rotor speed estimated by the VSG.

$$P^* - P_{load} - D(\omega_{vsg} - \omega_n) = J \omega_n \frac{d\omega_{vsg}}{dt}$$

(1)

In conventional synchronous generator scheme, $P^*$ indicates reference mechanical power input to the conventional synchronous generator that is provided by a speed
governor. In literature, this is mimicked with $P-\omega$ droop control. The control equation of this droop control is given in (2) where $P_0$ is the nominal value of active power, and $P_{load}$ is the measured value of active power delivered to load. Substituting for $P^*$ in (1) with that in (2), the resulting equation is shown in (3). Similarly, the reactive power loop is designed for VSG based on (4) where, $v_0^*$ and $v_{load}^*$ are the reference voltage to the voltage controller and voltage measured at the load terminals respectively; $Q_0$ and $Q_{load}$ are the nominal value of reactive power and reactive power measured at the load terminals respectively and $K_{q-v}$ is the droop gain coefficient of conventional reactive power — voltage ($Q-v$) droop control. In this conventional VSG, the relation between $K_{p-\omega}$ and $D_P$ is maintained as $K_{p-\omega} = \frac{1}{D_P}$.

$$P^* = P_0 - K_{p-\omega} (\omega_{vsg} - \omega_h)$$ (2)

$$P_0 - K_{p-\omega} (\omega_{vsg} - \omega_h) - P_{load} - D_P (\omega_{vsg} - \omega_h) = J \omega_h \frac{d\omega_{vsg}}{dt}$$ (3)

$$v_0^* = v_{load} - K_{q-v} (Q_0 - Q_{load})$$ (4)

3. Proposed Modified VSG and Design of its Parameters

From the guidelines and the control parameters of the VSG that are provided in [9–11], the procedure for designing the $K_{p-\omega}$ of proposed MVSG as shown in Figure 2, is consolidated as follows. With a maximum allowable active power variation $\Delta P_{max}$ of $62.8 \times 10^3$ W and the maximum allowable frequency deviation of 2%, the value of $D_P$ is calculated as shown in (5).

$$D_P = \frac{\Delta P_{max}}{\Delta \omega_{max}} = \frac{62.8 \times 10^3}{(2 \times \pi \times 50) \times 2\%} = 1 \times 10^{-4}$$ (5)

With a maximum allowable reactive power variation $\Delta Q_{max}$ of $21.02 \times 10^3$ Var, the base value of the apparent power is obtained as given in (6).

$$S_{base} = \sqrt{(\Delta P_{max})^2 + (\Delta Q_{max})^2} = \sqrt{(62.8 \times 10^3)^2 + (21.02 \times 10^3)^2} = 66.224 \times 10^3 \text{ VA}$$ (6)

For $K_{p-\omega} = 20pu$, its equivalent value is evaluated by solving (7).

$$K_{p-\omega} = 20pu = 20 \times \frac{S_{base}}{\omega_h} = [a] \times \left[ \frac{1}{D_P} \right]$$ (7)

With a nominal frequency of 50 Hz, $\omega_h = 2 \times \pi \times 50 = 314.7 \text{ rad/s}$, and ‘$a$’ is an equalizing constant whose value is obtained as shown by (8).

$$20 \times \frac{66.224 \times 10^3}{314.7} = [a] \times \left[ \frac{1}{1 \times 10^{-4}} \right] \Rightarrow a = 2.3759$$ (8)

![Figure 2](image-url)
A distinction in the active power loop structures between the conventional VSG and the proposed MVSG is apparent from Figure 2, in relation to the constant ‘a’. In conventional VSG, this value is held at 1, whereas for the proposed MVSG, it is set at 2.3759.

4. Simulation Results and Comparative Analysis

A single DGS-based microgrid system feeding two three-phase loads and connected to the utility grid at 440 V and 50 Hz is considered in this study. The total system is modelled in MATLAB. The inverter’s output is regulated by two control methodologies.

- Conventional Methodology: Traditional VSG scheme (with \( a = 1 \)) [10].
- Proposed Methodology: MVSG (with \( a = 2.3759 \)).

Load-1 is of a fixed and continuous type with 1200 W + j300 Var, whereas Load-2 is characterized as a momentary load occurring between 80 to 100 s. The transients linked with the activation and deactivation of Load-2 at 80 s and 100 s are denoted as T80 and T100, respectively. The loadings with Load-2 during test cases 1, 2, and 3 are 3000 W, 5000 W, and 10,000 W respectively. Table 1 presents the specific values of various control parameters associated with MVSG.

Table 1. Control parameters of MVSG.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Description</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>J</td>
<td>Equivalent rotor inertia (kg-m²)</td>
<td>56.3</td>
</tr>
<tr>
<td>( \omega_o )</td>
<td>Nominal angular frequency (rad/s)</td>
<td>314.7</td>
</tr>
<tr>
<td>( K_{p,o} )</td>
<td>Active power loop gain (pu)</td>
<td>20</td>
</tr>
<tr>
<td>D</td>
<td>Damping coefficient (pu)</td>
<td>17</td>
</tr>
<tr>
<td>( K_{r-v} )</td>
<td>Reactive power loop gain</td>
<td>( 1.48 \times 10^{-3} )</td>
</tr>
</tbody>
</table>

In test case 1 corresponding to T80, both methods brought the frequency below 51 Hz before 83 s, indicating stability. However, the conventional VSG method performed worse due to a smaller gap between the system frequency and 51 Hz at 83 s, as observed in Figure 3. In contrast, the proposed MVSG method showed better results. Similar findings applied to T100, with both methods lifting the frequency above 49 Hz before 103 s for stability. Again, the conventional VSG method had a weaker transient response than the proposed MVSG method, owing to smaller frequency deviations at 103 s (Figure 3a). In test case 2, using the proposed MVSG, the frequency reached stable below 51 Hz before 83 s (T80) and above 49 Hz before 103 s (T100). However, with conventional VSG, the frequency almost reached 51 Hz at 83 s (T80) and nearly 49 Hz at 103 s (T100), pushing the system towards instability. This is evident in the zoomed views of T80 and T100 in Figure 3b.
In Test Case 3, the system frequency with conventional VSG stayed above 51 Hz at 83 s, and below 49 Hz at 103 s under T80 and T100 indicating instability (Figure 3c). In contrast, the proposed MVSG ensured stability with good transient response during T80 and T100. The active power results under this test case 3 are shown in Figure 4a. From this, it is noticed that the conventional method exhibits a longer settling time than the proposed method. Further, from voltage results as noticed from Figure 4b and Table 2, for T80 and T100 transients, conventional VSG had a longer settling time (about 5 s) than proposed MVSG. Table 2 outlines a thorough comparison of both methods’ transient performance under various test cases involving frequency and voltage aspects, showcasing the best-performing method.

Figure 3. Frequency responses obtained with various test cases.

Figure 4. Active power and voltage profiles observed under test case 3.
Table 2. Comparison of conventional and proposed methods in various performance aspects.

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<tbody>
<tr>
<td>Frequency characteristics</td>
<td>Case-1</td>
<td>Maximum peak (Hz)</td>
<td>53.4</td>
<td>52.9</td>
<td>II</td>
<td>45.9</td>
<td>46.6</td>
<td>II</td>
</tr>
<tr>
<td></td>
<td>Case-2</td>
<td></td>
<td>54.8</td>
<td>54</td>
<td>II</td>
<td>44.2</td>
<td>45.2</td>
<td>II</td>
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<td>Case-3</td>
<td></td>
<td>57.7</td>
<td>56.3</td>
<td>II</td>
<td>40.8</td>
<td>43.2</td>
<td>II</td>
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<td></td>
<td>Case-1</td>
<td>Freq. at 83 s (Hz)</td>
<td>50.5</td>
<td>50.2</td>
<td>II</td>
<td>49.5</td>
<td>50</td>
<td>II</td>
</tr>
<tr>
<td></td>
<td>Case-2</td>
<td></td>
<td>51</td>
<td>50.2</td>
<td>II</td>
<td>49</td>
<td>50</td>
<td>II</td>
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<td></td>
<td>Case-3</td>
<td></td>
<td>52.1</td>
<td>50.5</td>
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<td>48.1</td>
<td>49.8</td>
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<td>Case-1</td>
<td>Settling time (s)</td>
<td>91.6</td>
<td>83.2</td>
<td>II</td>
<td>105</td>
<td>102</td>
<td>II</td>
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<td></td>
<td>Case-2</td>
<td></td>
<td>85.9</td>
<td>84.2</td>
<td>II</td>
<td>106</td>
<td>103</td>
<td>II</td>
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<td></td>
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<td>90.2</td>
<td>50.3</td>
<td>II</td>
<td>106</td>
<td>103</td>
<td>II</td>
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<tr>
<td>Voltage characteristics</td>
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<td>696</td>
<td>I &amp; II</td>
<td>667</td>
<td>667</td>
<td>II</td>
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<tr>
<td></td>
<td>Case-2</td>
<td></td>
<td>709</td>
<td>709</td>
<td>I &amp; II</td>
<td>658</td>
<td>661</td>
<td>II</td>
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<td>I &amp; II</td>
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<td>II</td>
<td>2.6</td>
<td>1.8</td>
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<td>2.3</td>
<td>II</td>
<td>5</td>
<td>2.4</td>
<td>II</td>
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</tbody>
</table>

5. Conclusions

From Table 3, it is found from test case 3 that the conventional VSG scheme took around 5 s to come down below the allowable limit of 51 Hz. This made the system vulnerable to unnecessary tripping. Based on a detailed study on the design of MVSG and the impact of various parameters on the transient performance, it is identified that an equalizing factor, ‘a’ plays a vital role in improving the stability and response. Conventional VSG utilizes ‘a = 1’ in the design of VSG. From a critical understanding of the design of the VSG, it is identified that ‘a = 2.3759’ is the appropriate value that should be used in the design of the MVSG for the system parameters considered in this work. This MVSG scheme performance in test case 3 has caused the frequency to come below the allowable limit in 2.2 s, which is less than the 3 s time delay. In terms of frequency settling time, the proposed MVSG method has outperformed the conventional VSG method. The superiority of the proposed method is further confirmed in test case 3 through a lesser frequency dip than the conventional method.

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