

Sliding Mode Control of a Photovoltaic Water Pumping System

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Session: Energy Systems



Ministère de l'Enseignement Supérieur
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MOTIVATION



Burning fossil fuels



Greenhouse gas emissions



Climate change

Photovoltaic water pumping system



Solar energy





Use of renewable energies

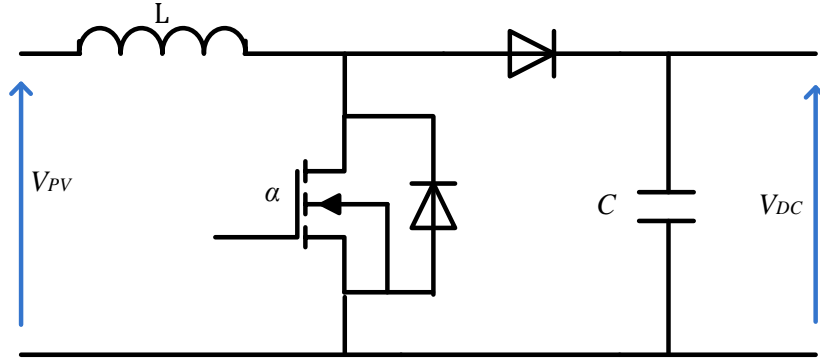


OBJECTIVES



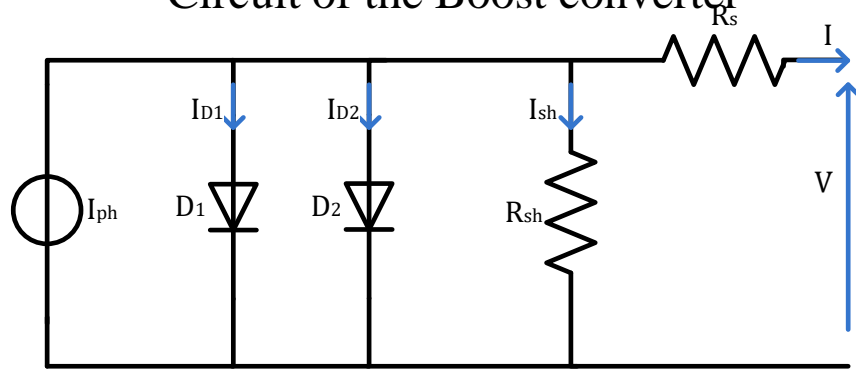
-  **To present a three-phase double-stage photovoltaic water pumping system based on three levels Neutral Point Clamped inverter.**
-  **To use the Sliding Mode controller instead the classical Proportional–Integral controller**

METHODOLOGY



Circuit of the Boost converter

$$V_{dc} = \frac{V_{PV}}{1 - \alpha}$$



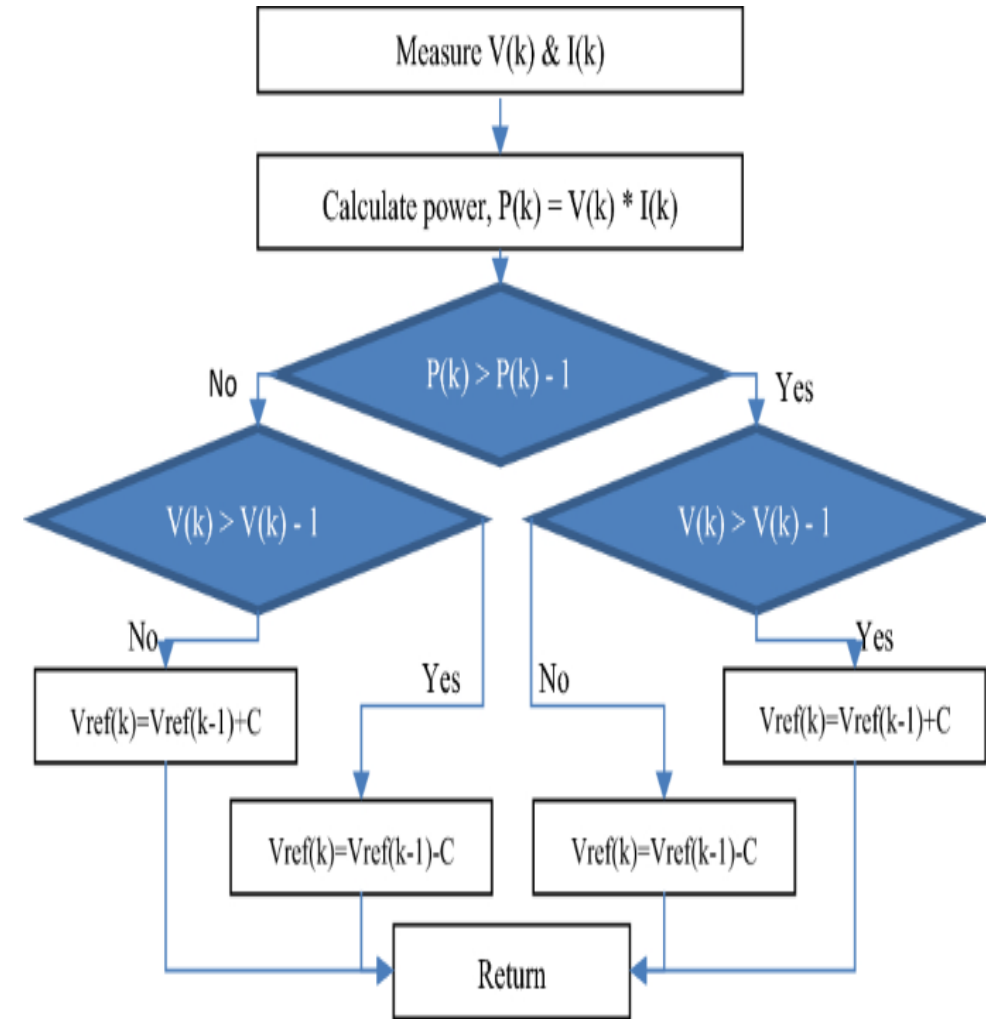
Photovoltaic cell electrical model

$$I = I_{ph} - (I_{D1} + I_{D2}) - I_{sh}$$

With:

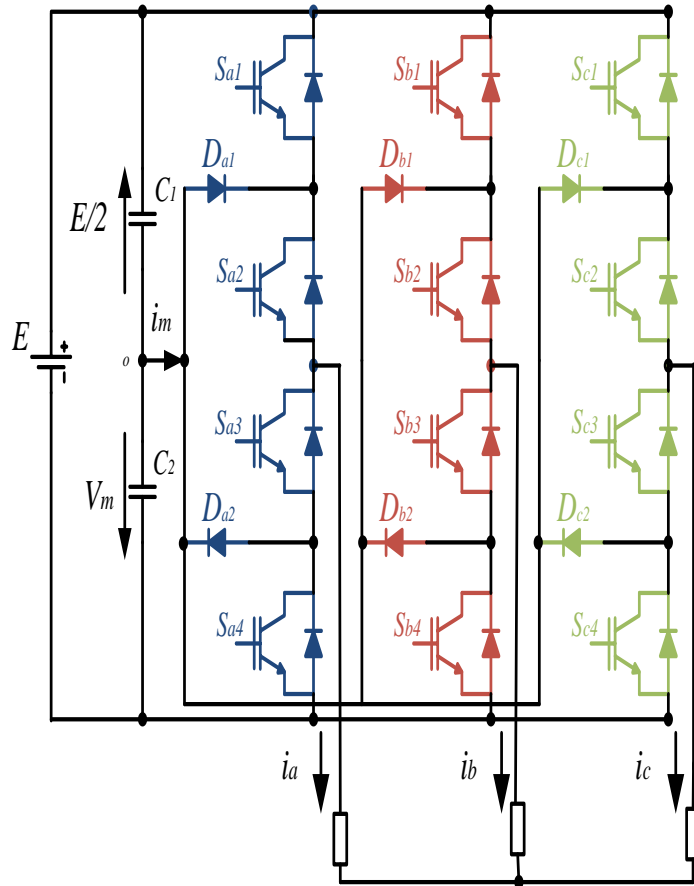
$$I_{D1} = I_1 \times \left[e^{\frac{q \cdot (v + R_s \cdot I)}{A \cdot K \cdot T}} - 1 \right] \quad \text{and} \quad I_{D2} = I_2 \times \left[e^{\frac{q \cdot (v + R_s \cdot I)}{A \cdot K \cdot T}} - 1 \right]$$

$$I_{sh} = \left(\frac{V + R_s \cdot I}{R_{sh}} \right)$$

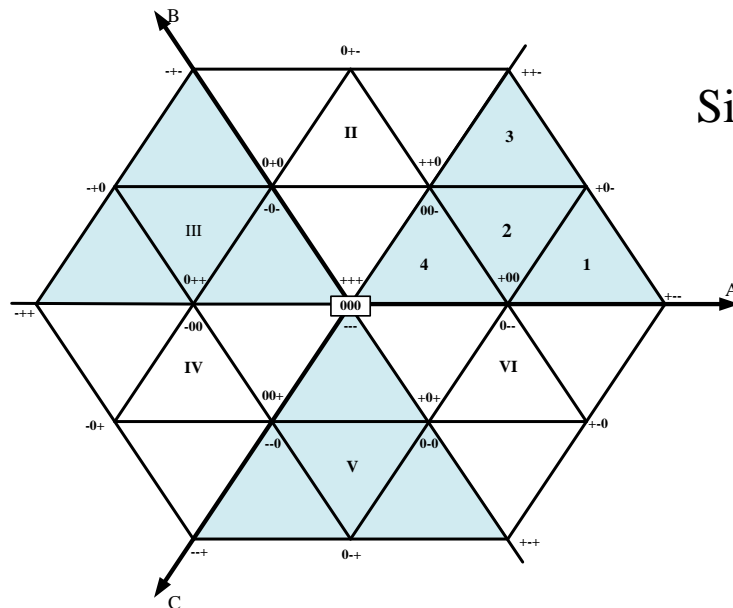
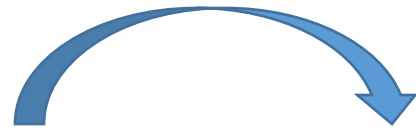


Perturb and Observe MPPT flowchart

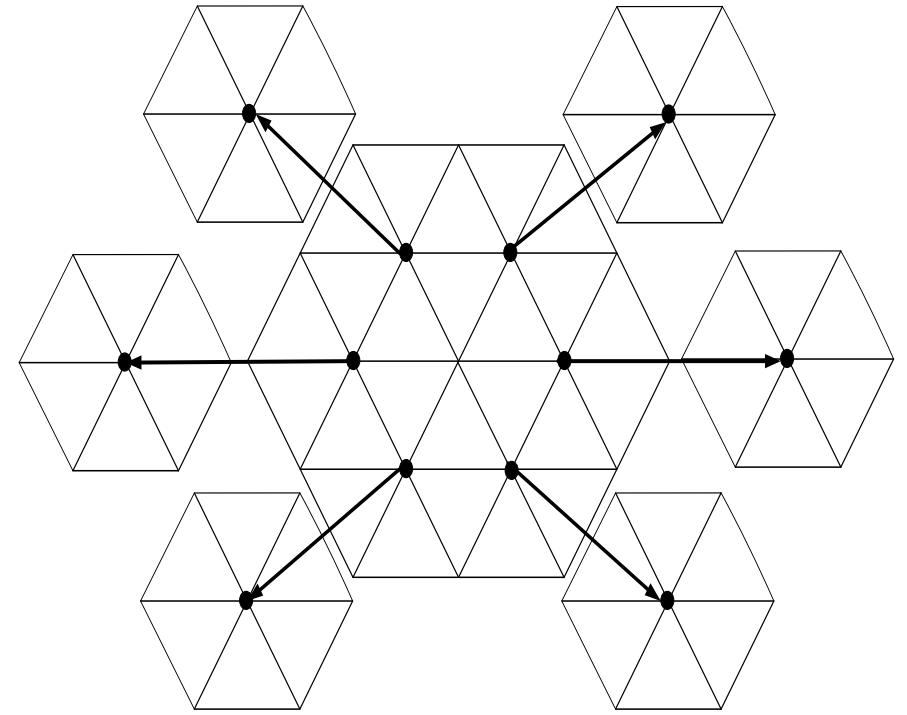
METHODOLOGY



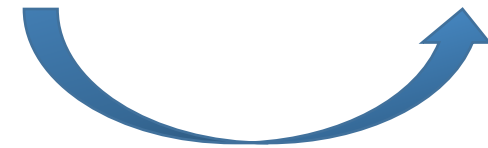
Three-level NPC inverter circuit



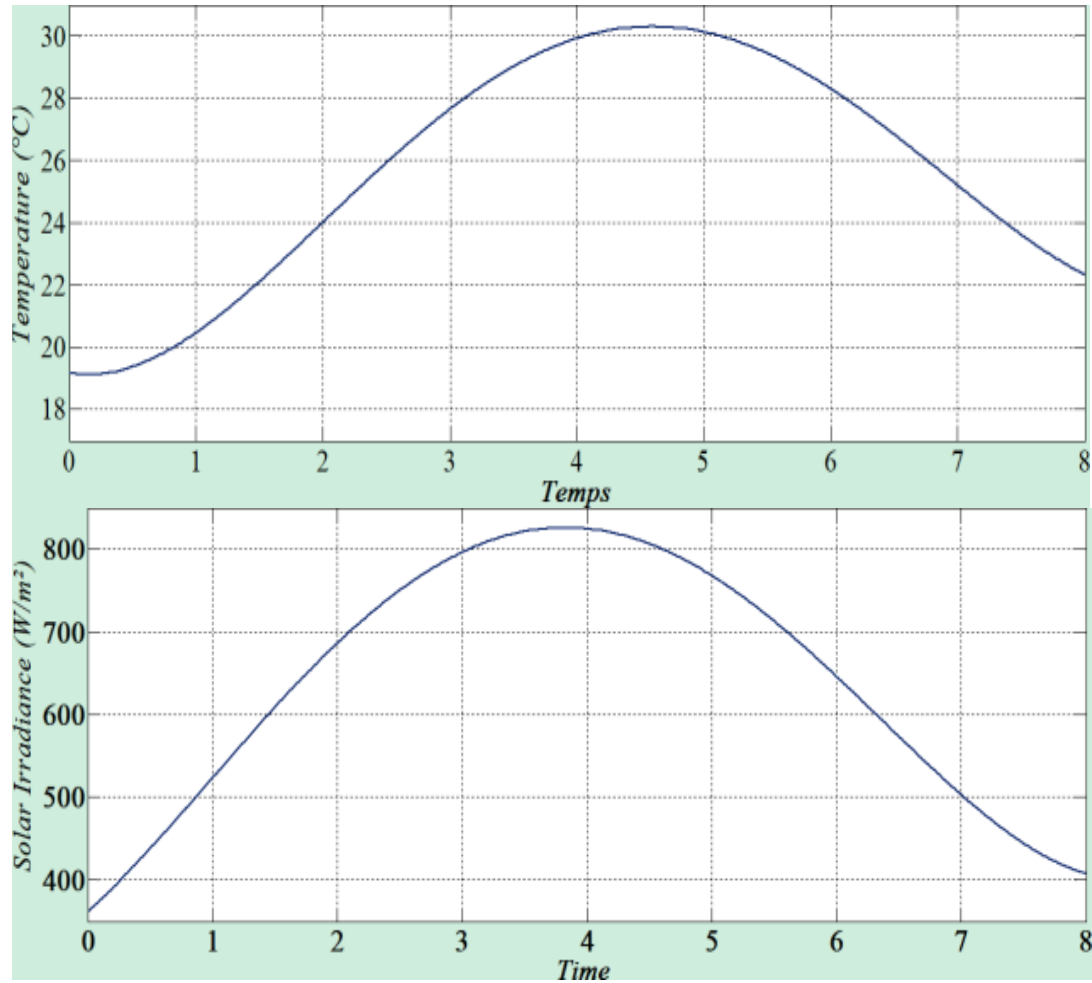
Three-level space vector diagram



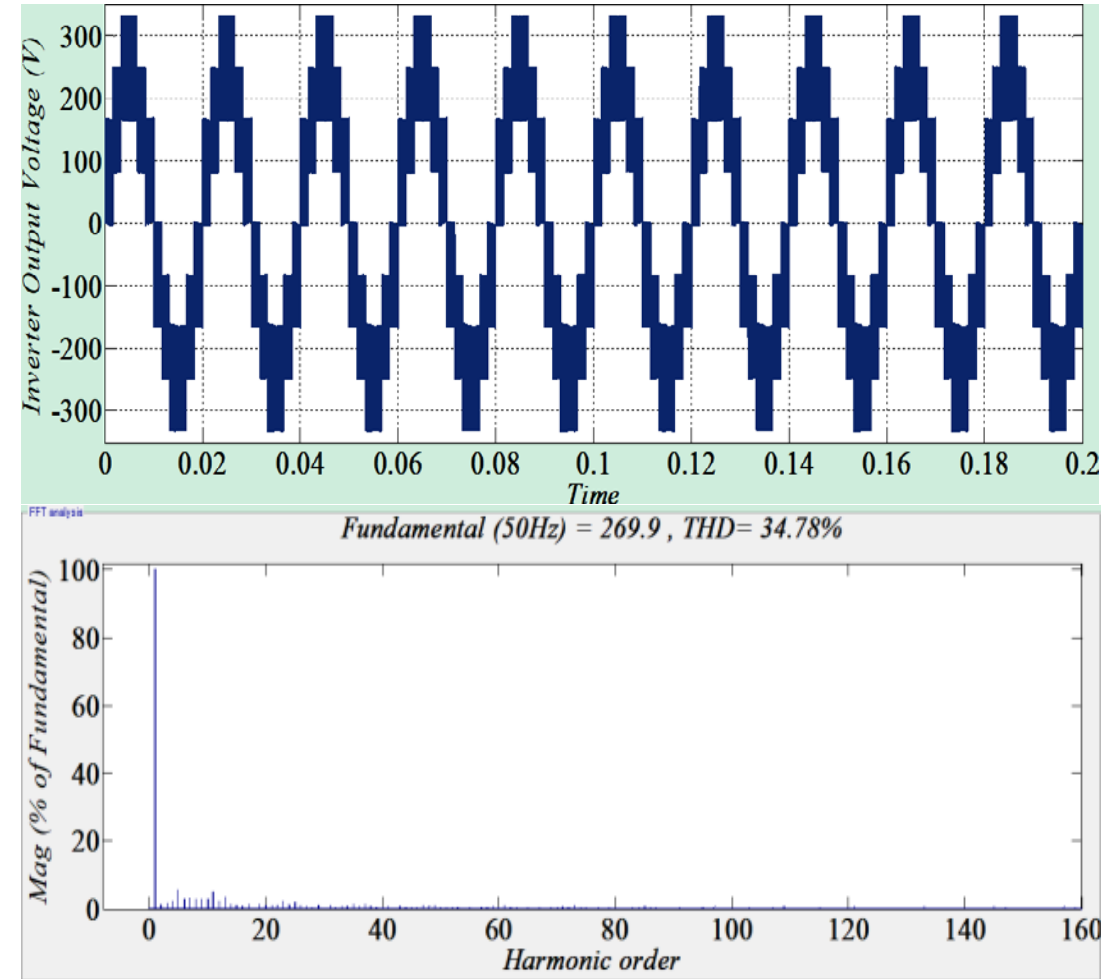
Simplification three-level space vector diagram



RESULTS

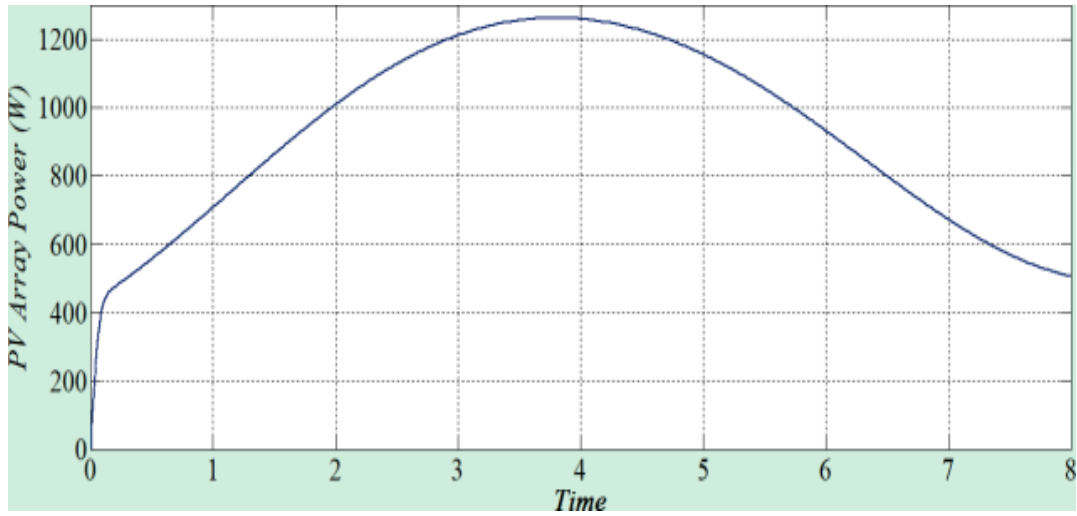


Variations of solar irradiance and temperature

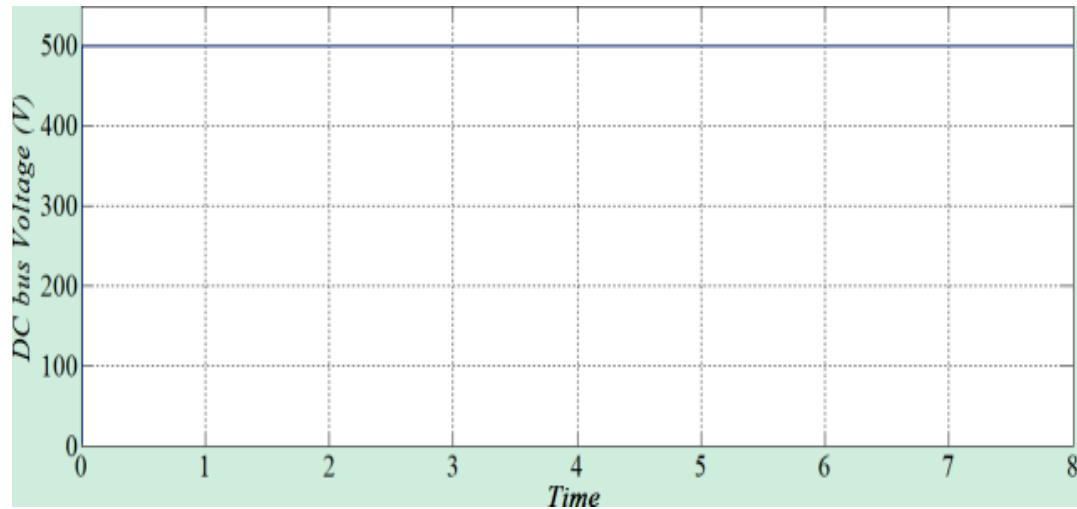


Output voltage of the NPC inverter and its spectral analysis

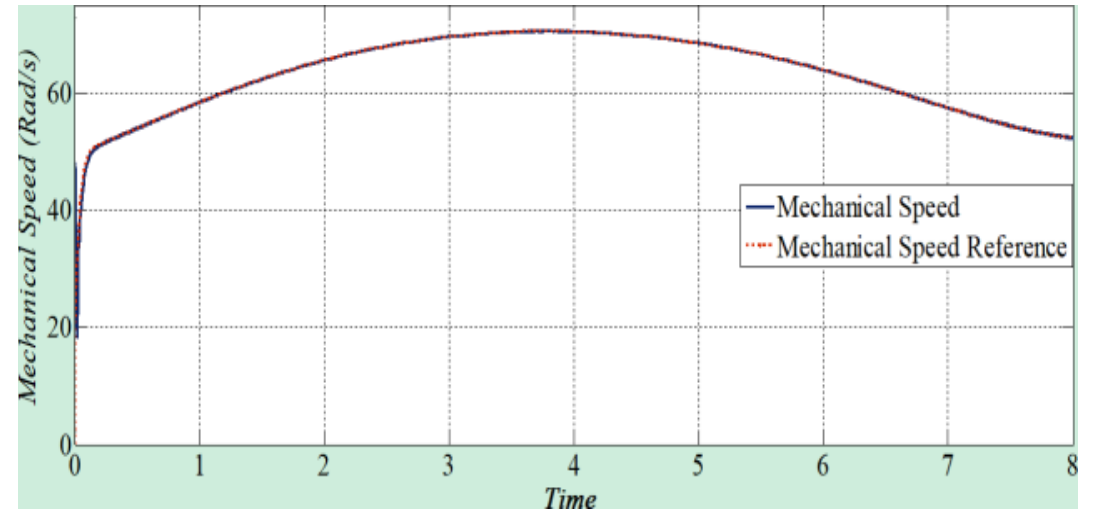
RESULTS



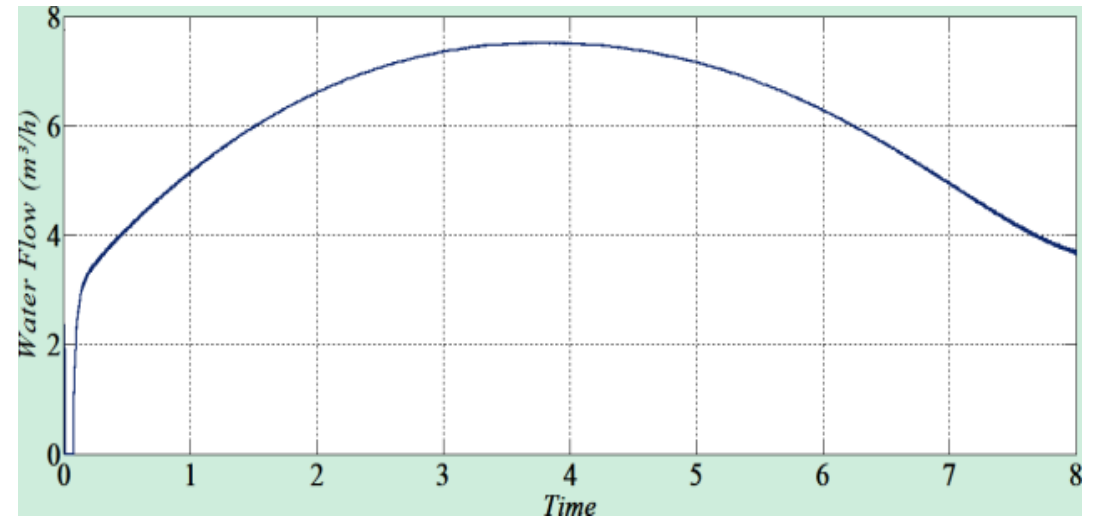
Power generated by photovoltaic panels



DC bus voltage



Rotation speed of the pump and its reference



Flow of water pumped

CONCLUSIONS



- ❑ The proposed control scheme features several advantages such as the generation of high-quality voltage, the capacity to operate at a lower switching frequency than a two-level.**
- ❑ The proposed Sliding Mode Control of our system showed its effectiveness and robustness against variations in parameters influencing PV systems.**
- ❑ The inverter can be easily expanded by increasing the levels. Thus, number of the output levels is increased and the inverter generates higher-quality output voltage.**
- ❑ The proposed system produces less dv/dt stresses imposed on the switching devices and generates fewer harmonic in voltage and current.**

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