



Proceeding Paper

Impact of Electrical Noise on the Accuracy of Resistive Sensor Measurements Using Sensor-to-Microcontroller Direct Interface [†]

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Abstract

Wireless sensor networks (WSNs) implemented in the paradigm of the Internet of Things (IoT) are characterized by a large number of distributed sensor nodes that make measurements in-the-field and communicate with other sensor nodes and servers in the cloud by means of wireless technology. Sensor-to-microcontroller direct interface (SMDI) is a technique used for the measurement of resistive sensors without the use of an ADC. In SMDI based measurements, the sensor is directly interfaced with the digital input-output pins of the general purpose input output (GPIO) interface of microcontrollers and FPGAs. Compared with the measurements per-formed with an ADC, SMDI is characterized by lower cost and lower power consumption. In this paper, the impact of noise on the accuracy of resistive sensor measurements using SMDI is investigated. The study was carried out by LTSpice electrical level simulations and validated by preliminary experimental measurements, where a set of resistances in the range from 100Ω to $10 k\Omega$ were measured by SMDI under different levels of noise. For each operative condition, the simulations were also carried out in the case of measurements performed with a 12-bit ADC and the achieved accuracy for the measured resistances was compared with the results achieved by SMDI. The results have shown that noise can seriously impact the measured accuracy of resistive sensors by SMDI and, differently from the ADC measurements, the accuracy cannot be improved by averaging on multiple measurements. A mitigation strategy to estimate the noise level and to improve the measurement accuracy of resistive sensors by SMDI was also proposed.

Keywords: resistive sensors; microcontrollers; ADC; simulations; sensor direct interface; noise; GPIO

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1. Introduction

Sensors are devices capable of accurately measuring different types of quantities and are normally used in different types of applications, such as environmental monitoring [1–4], food quality and safety analysis [5–10], structural health monitoring of civil infrastructures [11–14], microbial contamination detection [15–19], and industry 4.0

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applications [20–23]. In particular, the integration of low-power computing devices, sensors, and wireless communication technologies has led to the development of wireless sensor networks (WSNs), where large amount of data are acquired and shared with other sensor nodes and servers in the cloud [24–27]. Sensor data are normally acquired by microcontrollers or Field Programmable Gate Arrays (FPGAs) using analog-to-digital converters (ADCs), either integrated in the computing device or external [28–31].

WSNs are usually powered by batteries and/or energy harvesting devices, thus the sensor node power consumption is of paramount importance [32–34]. From this point of view, sensor-to-microcontroller direct interface (SMDI) is a technique that can be used to acquire sensor data without the use of an ADC, thus providing significant advantages in terms of low power consumption [35,36]. SMDI exploits the Schmitt triggers integrated in the general purpose input output (GPIO) interface of microcontrollers and FPGAs to directly interface the sensors analog output signal with the digital input pins of the computing device. SMDI has been exploited for the measurement of different types of sensors, such as resistive sensors [37–40], capacitive sensors [41–44], inductive sensors [45–47], as well as sensors featuring an output voltage signal [48].

As known, noise affects the reliability of electronic systems and can produce a severe limitation of the system reliability [49,50]. In particular, in the case of sensors, the noise contributes to the decrease of the signal-to-noise ratio and the measurement accuracy. Thus, we have investigated the impact of the noise on the measurement accuracy of resistive sensors using the SMDI technique. Different types of electrical noise can impact the reliability of electronic circuits, and they are typically classified in two different groups: intrinsic (or internal) noise and external noise [51]. Intrinsic noise refers to all the noise types that are generated inside an electronic device, such as thermal noise (produced by the random thermal agitation of electrons and holes in a conductor), shot noise (produced by the random arrival of electrons and holes at a discontinuous interface inside a device), flicker noise (that is usually found in transistors operating at low frequency), and transit time noise (produced by the energy transfer between electrons and ions). External noise, instead, refers to all types of noise that are generated outside of electronic devices, such as crosstalk noise and electromagnetic noise. Another classification for noise types is related to the shape of their power spectral density (PSD), as a function of frequency (f). White noise presents a flat PSD, while pink noise features a PSD proportional to f⁻¹, red noise features a PSD proportional to f⁻², blue noise features a PSD proportional to f, and violet noise features a PSD proportional to f². Our study considers only white noise as electrical noise source. The study was carried out by electrical level simulations with LTSpice [51] (with validation on preliminary experimental measurements), using a set of standard resistors of value between 100 Ω and 10 k Ω to represent a realistic working range for the values of a resistive sensor, and comparing the results with the case of measurements carried out using a 12-bit ADC for reference. The results have shown that measurements carried out using SMDI are strongly affected by noise and different noise types (gaussian white noise or uniform white noise) can have a different impact on the measurement accuracy. Moreover, the presence of noise can seriously degrade the measurement accuracy also in the case the measured sensor value is averaged on a large number of samples. In order to address these issues, we have proposed a mitigation strategy that can compensate the impact of noise on the measurement accuracy. It is based on the idea to use a programmable potentiometer in order to compensate the impact of noise by performing periodic calibrations on its value.

The paper is structured as follows. In Section 2, the simulation setups for the resistive sensor measurements using the SMDI technique and the 12-bit ADC are presented. In Section 3, the simulation results are presented and the accuracy achieved with the SMDI-based measurements and the ADC-based measurements are compared. In Section 4, the

simulation results are discussed and a mitigation strategy to improve the measurement accuracy for the SMDI technique is presented. In Section 5, the simulations results are validated by experimental measurements on a microcontroller. Finally, conclusions are presented in Section 6.

2. Simulation Setup

The study has been carried out by electrical level simulations with LTSpice [52], considering a 180 nm CMOS technology. In Section 2.1, the simulation setup for the case of SMDI-based measurements is presented, while in Section 2.2, the simulation setup for the case of ADC-based measurements is presented.

2.1. SMDI-Based Measurements

Sensor-to-microcontroller direct interface (SMDI) is a technique that can be used to acquire sensor data without the use of an ADC, by interfacing the sensor directly to digital input-output pins of a microcontroller. The simulation setup for a resistive sensor R_T, measured with a microcontroller using the SMDI technique, is shown in Figure 1, where C is a discrete external capacitance.

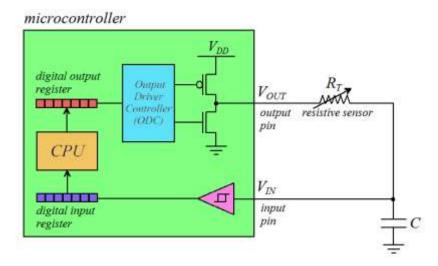


Figure 1. Simulation setup for a resistive sensor R_T measured using the SMDI technique.

SMDI exploits the Schmitt triggers integrated in the GPIO interface of microcontrollers to create an astable oscillator (with time constant R_TC) whose period is measured, using the digital timers integrated in the microcontroller, to estimate the resistive sensor value R_T . The analog voltage V_{IN} on an input pin of the microcontroller is fed to the non-inverting Schmitt trigger integrated in the GPIO interface of the microcontroller. The Schmitt trigger features two threshold voltages (V_H and V_L) so that, if $V_{IN} > V_H$ then the Schmitt trigger output is V_{DD} , while if $V_{IN} < V_L$ then the Schmitt trigger output is V_{DD} , while if $V_{IN} < V_L$ then the Schmitt trigger output is V_{DD} .

The microcontroller CPU acquires the digital value at the Schmitt trigger output and controls the Output Driver Controller (ODC) module so that the voltage at the output pin (Vout) is the complement of the Schmitt trigger output value.

The working principle of the measurement of R_T using SMDI can be defined as follows. Initially, it is $V_{IN} = V_L$, $V_{OUT} = V_{DD}$, and the capacitance C is charged with time constant R_TC (i.e., V_{IN} increases). When it is $V_{IN} = V_H$, the Schmitt trigger output switches from 0 V to V_{DD} and V_{OUT} switches from V_{DD} to 0 V. Then, the charging step of the capacitance (whose duration is indicated with t_H) terminates and the discharging of the capacitance C starts. When it is $V_{IN} = V_L$, the Schmitt trigger output switches from V_{DD} to 0 V and V_{OUT} switches from 0 V to V_{DD} . Then, the discharging step of the capacitance (whose duration

is indicated with t_L) terminates and the charging of the capacitance C starts again. The values of t_H and t_L can be expressed as [40]:

$$t_{H} = R_{T}C \int_{V_{I}}^{V_{H}} \frac{1}{V_{DD} - V_{IN}} dV_{IN} = R_{T}C \cdot \log \frac{V_{DD} - V_{L}}{V_{DD} - V_{H}}$$
(1)

$$t_{L} = -R_{T}C \int_{V_{H}}^{V_{L}} \frac{1}{V_{IN}} dV_{IN} = R_{T}C \cdot \log \frac{V_{H}}{V_{L}}$$
 (2)

The oscillation period T_P of the signals V_{IN} and V_{OUT} can be measured using the digital timers integrated in the microcontroller and can be expressed as:

$$T_{P} = t_{H} + t_{L} = R_{T}C \cdot \log \frac{V_{H}(V_{DD} - V_{L})}{V_{L}(V_{DD} - V_{H})}$$
(3)

and the value of the resistive sensor can be calculated as:

$$R_{T} = \frac{T_{P}}{C \cdot \log \frac{V_{H}(V_{DD} - V_{L})}{V_{L}(V_{DD} - V_{H})}} \tag{4}$$

The simulation setup of Figure 1 was implemented in LTSpice by using an ideal non inverting Schmitt trigger with threshold voltages of V_H = 1.9 V and V_L = 1.4 V, a power supply of VDD = 3.3 V, a value for the capacitance C of 100nF, seven different values for the resistive sensor R_T (100 Ω , 250 Ω , 500 Ω , 1000 Ω , 2500 Ω , 5000 Ω , 10,000 Ω). The choice of fixed Schmitt trigger thresholds ($V_H = 1.9 \text{ V}$ and $V_L = 1.4 \text{ V}$) has been considered as a case study. As discussed in [48], these thresholds can present significant different values for different devices. Moreover, slight differences in the Schmitt trigger thresholds for a single device also occur due to parameters dispersion introduced during manufacturing. Thus, to make accurate estimations of the sensor value, the exact values of the Schmitt trigger thresholds must be determined before the measurements. The operations performed by the microcontroller CPU were emulated by connecting a NOT digital gate between the output of the Schmitt trigger and the microcontroller output pin. The impact of the noise on the sensor measurement accuracy was evaluated by placing a white noise voltage generator (uniformly distributed) in series with the input pin of Figure 1. Nine different values of the white noise peak-to-peak voltage were evaluated (1.25 mV, 2.5 mV, 5 mV, 10 mV, 20 mV, 33.3 mV, 40 mV, 50 mV, 100 mV).

2.2. ADC-Based Measurements

The standard technique for sensors measurement by a microcontroller is based on the use of an ADC (integrated in the microcontroller or external) to acquire the analog information from the sensor and translate it to a digital format for data processing. In the case of a resistive sensor, a typical measurement setup is presented in Figure 2.

The measurement setup shown in Figure 2 exploits a Wheatstone bridge to generate a differential voltage (V_{IN^+} – V_{IN^-}) that is used to estimate the resistive sensor value R_T . Indicated with R_{REF} a reference resistor of known value, it is:

$$V_{IN+} = \frac{R_T}{R_T + R_{PFF}} V_{DD} \tag{5}$$

Since it is $V_{IN-} = V_{DD}/2$, then it is:

$$V_{IN+} - V_{IN-} = \frac{V_{DD}}{2} \frac{R_T - R_{REF}}{R_T + R_{RFF}} \tag{6}$$

The differential voltage V_{IN+} – V_{IN-} is sampled and quantized by the ADC and a digital word $D_{11}D_{10}$... D_1D_0 is generated and fed to the microcontroller for data processing.

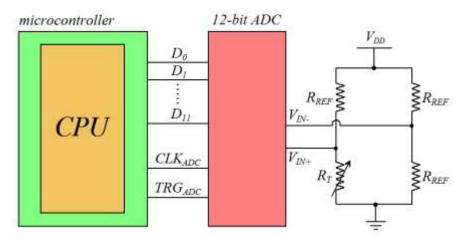


Figure 2. Simulation setup for a resistive sensor R_T measured using a 12-bit ADC.

The simulation setup shown in Figure 2 has been implemented in LTSpice considering a 12-bit differential input ADC (LTC2311-12) [53], a power supply of V_{DD} = 3.3 V, a value for the reference resistor R_{REF} of 1 k Ω , seven different values for the resistive sensor R_T (100 Ω , 250 Ω , 500 Ω , 1000 Ω , 2500 Ω , 5000 Ω , 10,000 Ω). The impact of the electrical noise on the sensor measurement accuracy has been evaluated by placing a white noise voltage generator (uniformly distributed) between the node V_{IN+} and the non-inverting input of the ADC in Figure 2. Nine different values of the white noise peak-to-peak voltage were evaluated (1.25 mV, 2.5 mV, 5 mV, 10 mV, 20 mV, 33.3 mV, 40 mV, 50 mV, 100 mV). The characteristic of the 12-bit ADC output as function of the resistive sensor value R_T in the case of noise-free operating condition is presented in Figure 3, where $V_{OUT,ADC}$ represents the analog equivalent of the ADC digital output $D_{11}D_{10}$... D_1D_0 . As can be seen, the characteristic is quasi-linear, with deviations from the linear behavior when the sensor resistance R_T deviates from the reference resistance R_{REF} of about one order of magnitude.

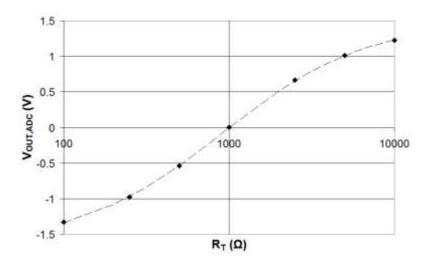


Figure 3. Characteristic of the 12-bit ADC output as function of R_T .

3. Simulation Results

The accuracy achieved in the case of a resistive sensor R_T has been evaluated for both the SMDI-based measurements and the ADC-based measurements according to the operative conditions described in Section 2.1 and Section 2.2, respectively. The measured R_T

has been evaluated in terms of average value and standard deviation, by carrying out 100 simulations for each operative condition. The simulation results for the case of the SMDI-based measurements are reported in Section 3.1, while the simulations results for the case of the ADC-based measurements are reported in Section 3.2.

3.1. SMDI-Based Measurements

The average value of the measured resistive sensor R_T is plotted in Figure 4 as function of the peak-to-peak voltage noise ($V_{\text{noise},PP}$), for the case of a sensor resistance of nominal value 5 k Ω . As can be seen, the average value of measured R_T decreases linearly with the increase of $V_{\text{noise},PP}$ and deviates from its nominal value as $V_{\text{noise},PP}$ increases.

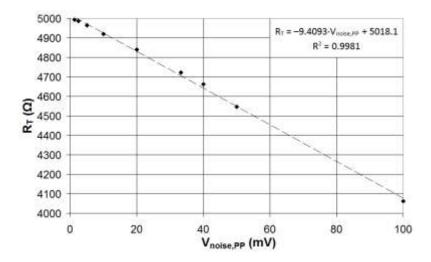


Figure 4. Average value of the measured R_T as function of the peak-to-peak voltage noise in the case of a sensor of nominal value 5 k Ω .

The measured values of R_T are presented in Table 1, in terms of average value (μ) and standard deviation (σ), as function of the nominal value of the sensor resistance and the peak-to-peak voltage noise.

Table 1. Measured values of R_T (average value and standard deviation) using the sensor-to-micro-controller direct interface technique in the case of different nominal values for the sensor resistance and for different values of the noise level.

	R _T (Ω)													
	100		250		500		1000		2500		5000		10,000	
Vnoise,PP (mV)	μ	σ	μ	σ	μ	σ	μ	σ	μ	σ	μ	σ	μ	σ
1.25	101.00	0.08	251.04	0.21	500.89	0.35	1000.5	0.64	2498.4	1.04	4993.6	1.66	9984.5	3.65
2.5	100.99	0.16	250.88	0.38	500.49	0.56	999.01	1.03	2494.2	1.78	4985.6	3.39	9965.6	5.31
5	100.91	0.27	250.68	0.61	498.84	0.87	996.33	1.70	2484.4	3.10	4963.8	4.47	9925.4	7.05
10	100.68	0.49	249.12	0.93	496.43	1.48	989.59	2.72	2465.9	4.39	4919.6	7.79	9835.8	12.9
20	99.41	0.73	246.09	1.54	488.47	2.01	971.34	3.28	2417.0	7.08	4840.7	10.8	9644.9	19.6
33.3	97.95	0.89	242.02	2.33	476.36	2.38	950.23	6.57	2366.3	11.9	4723.0	20.0	9416.5	32.7
40	97.18	1.18	237.50	2.14	472.03	3.92	941.65	5.42	2331.8	13.3	4661.1	18.4	9302.2	24.9
50	95.99	1.70	234.42	3.09	464.78	4.35	925.23	7.40	2286.8	16.4	4546.8	18.5	9100.2	34.1
100	88.49	2.67	214.40	4.68	424.73	6.46	841.96	8.53	2064.6	29.0	4062.7	19.5	8154.9	46.5

As can be seen, as $V_{\text{noise,PP}}$ increases, the average value of the measured R_T decreases and deviates from its nominal value, while the standard deviation increases. Thus, in the case of high levels of noise, even if the measured value of R_T is averaged on a large number of measurements, a good level of accuracy cannot be achieved.

The relative error (in percent) of the measured resistance R_T is evaluated using the parameter Δ_{ERROR} , that is defined by:

$$\Delta_{ERROR} = 100 \cdot \left| \frac{R_{T,meas} - R_T}{R_T} \right| \tag{7}$$

where $R_{T,meas}$ is the measured value of the sensor while R_T is its nominal value.

The obtained values of Δ_{ERROR} are presented as function of the nominal value of R_T in Figure 5 for the case of the minimum level of electrical noise ($V_{noise,PP} = 1.25 \text{mV}$), and in Figure 6 for the case of the maximum level of noise ($V_{noise,PP} = 100 \text{ mV}$).

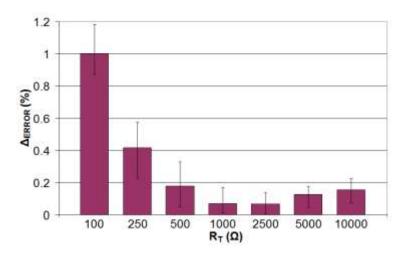


Figure 5. Measured Δ_{ERROR} as function of the nominal value of R_T for SMDI-based measurements in the case of the minimum level of noise ($V_{noise,PP}$ = 1.25 mV).

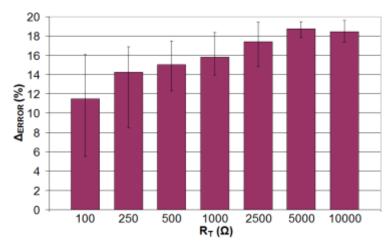


Figure 6. Measured Δ_{ERROR} as function of the nominal value of R_T for SMDI-based measurements in the case of the maximum level of noise ($V_{noise,PP} = 100 \text{ mV}$).

Data presented in Figures 5 and 6 confirms the results of Table 1. In the case of low levels of noise ($V_{noise,PP}$ = 1.25 mV), the SMDI technique achieves a very good accuracy with a relative error always lower than 1%, and even lower than 0.2% for values of R_T of 500 Ω or higher. In the case of high levels of noise ($V_{noise,PP}$ = 100 mV), instead, the achieved relative error is always very high (>10%).

3.2. ADC-Based Measurements

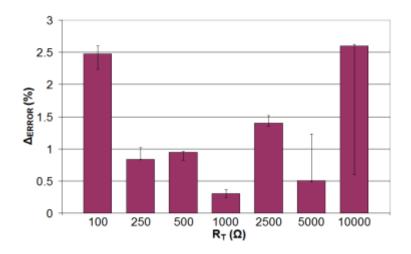
The measured values of R_T are presented in Table 2, in terms of average value (μ) and standard deviation (σ), as function of the nominal value of the sensor resistance and the peak-to-peak voltage noise. As can be seen, similarly to the case of SMDI-based measurements, the measured R_T standard deviation increases with the level of noise. However, differently from SMDI-based measurements, the average value of the measured R_T is almost independent of the noise level. Thus, in the case of ADC-based measurements, accurate values of the resistive sensor value can be achieved also in a noisy environment, if R_T is calculated by averaging on a large number of measurements.

Table 2. Measured values of R_T (average value and standard deviation) using the 12-bit ADC LTC2311-12 in the case of different values for the sensor resistance and for different values of the noise level.

	$R_{T}\left(\Omega ight)$													
	100		250		500		1000		2500		5000		10,000	
Vnoise,PP (mV)	μ	σ	μ	σ	μ	σ	μ	σ	μ	σ	μ	σ	μ	σ
1.25	102.48	0.16	247.91	0.10	495.26	0.19	1003.0	0.64	2534.9	1.82	5025.3	4.41	9739.9	20.2
2.5	102.45	0.22	247.86	0.28	495.32	0.35	1003.0	0.64	2535.3	1.96	5027.2	5.94	9742.2	27.8
5	102.43	0.41	247.84	0.47	495.31	0.61	1003.0	1.02	2535.5	3.07	5027.2	9.16	9741.3	34.3
10	102.40	0.83	247.79	0.91	495.23	1.19	1002.9	1.91	2535.3	5.55	5026.7	16.0	9740.2	52.5
20	102.31	1.62	247.70	1.82	495.18	2.31	1002.8	3.83	2534.8	10.9	5026.2	30.8	9737.0	98.6
33.3	102.16	2.76	247.55	3.06	495.05	3.84	1002.7	6.32	2534.5	17.9	5025.7	50.8	9737.0	159
40	102.10	3.31	247.52	3.70	495.01	4.62	1002.5	7.59	2534.3	21.4	5025.5	60.4	9734.5	190
50	102.01	4.14	247.44	4.63	494.89	5.79	1002.5	9.42	2534.1	26.4	5024.8	74.9	9732.8	236
100	101.61	8.28	247.03	9.23	494.55	11.6	1002.1	18.9	2533.3	53.5	5022.5	152	9740.8	470

The values of the relative error (Δ_{ERROR}), as defined in Equation (7), are presented as function of the nominal value of R_T in Figure 7 for the case of the minimum level of noise ($V_{noise,PP}$ = 1.25 mV) and in Figure 8 for the case of the maximum level of noise ($V_{noise,PP}$ = 100mV).

As can be seen, in the case of low levels of noise ($V_{noise,PP}$ = 1.25 mV), measurements carried out by the SMDI technique are more accurate (average Δ_{ERROR} of 0.28%) than the ADC-based measurements (average Δ_{ERROR} of 1.29%). On the contrary, in the case of high levels of noise ($V_{noise,PP}$ = 100 mV), measurements carried out by the SMDI technique are less accurate (average Δ_{ERROR} of 15.88%) than the ADC-based measurements (average Δ_{ERROR} of 3.27%).



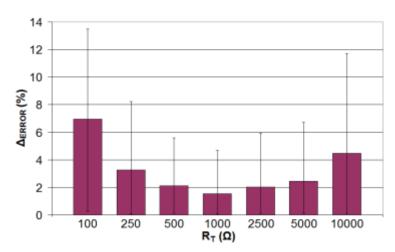


Figure 7. Measured Δ_{ERROR} as function of the nominal value of R_T for ADC-based measurements in the case of the minimum level of noise ($V_{noise,PP}$ = 1.25 mV).

Figure 8. Measured Δ ERROR as function of the nominal value of R_T for ADC-based measurements in the case of the maximum level of noise ($V_{noise,PP} = 100 \text{ mV}$).

4. Discussion

The results presented in Section 3 have shown that, as expected, higher levels of noise degrade the accuracy of sensor measurements, both in the case of ADC-based measurements and SMDI-based measurements. In the case of ADC-based measurements, however, the average value of sensor data is almost unaffected by the level of noise, thus an higher measurement accuracy can be achieved by averaging on a large number of samples. This is not the case for SMDI-based measurements, where the average value of the sensor resistance deviates from its nominal value as the noise level increases, thus preventing the possibility to improve the measurement accuracy by averaging on multiple samples. In this section, the reasons for the strong impact of noise on the accuracy of SMDI-based measurements are investigated, and a possible mitigation strategy to improve the accuracy of SMDI-based measurements in a noisy environment will be presented.

The waveforms of the voltage signals acquired during a simulation of the resistive sensor measurement with SMDI are presented in Figure 9. With reference to Figure 1, the voltage $V_{\rm IN}$ (input of the Schmitt trigger integrated in the microcontroller GPIO interface) is presented in the case of a signal with high electrical noise level (100 mV peak-to-peak amplitude) as well as in the case of the noise free signal.

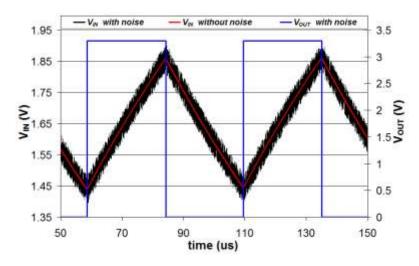


Figure 9. Waveforms of the voltage signals acquired during a simulation of the resistive sensor measurement using SMDI, in presence and absence of noise.

As can be seen, the presence of the noise results in the triggering of the Schmitt trigger thresholds (V_H and V_L) before the noise free voltage signal reaches these thresholds. This results in an increase of the detected V_L and a decrease of the detected V_H , and thus in a decrease of the measured period T_P as defined by Equation (3). Then, in accordance with Equation (4), this results in a decrease of the measured sensor resistance R_T , as also shown in the results of Section 3.1 (Figure 4 and Table 1). Moreover, the impact of the noise on the measurement accuracy is higher in the case of slower variations of the signal V_{IN} , thus in the case of higher nominal values of R_T , as also shown in Figure 6.

To investigate how different types of noise impact on the accuracy of SMDI-based measurements, we have evaluated the detected average values of the Schmitt trigger thresholds (V_L and V_H) during a SMDI measurement under different levels of electrical noise, for R_T = $10k\Omega$, and two different types of noise: uniformly distributed white noise signal and Gaussian distributed white noise signal. The results are shown as function of the noise standard deviation (σ_{noise}) in Figure 10 for the voltage threshold V_L and in Figure 11 for the voltage threshold V_H , with the average values of V_L and V_H calculated by averaging on 1000 measurements. As expected, the threshold voltage V_L increases with noise level, while the threshold voltage V_H decreases with noise level. Moreover, the amount of deviation of the detected Schmitt trigger threshold voltages from their nominal value changes for different types of noise: the deviation is stronger for a Gaussian distributed white noise than for a uniformly distributed white noise.

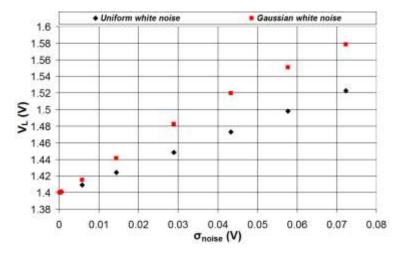


Figure 10. Average value of the detected Schmitt trigger threshold V_L as function of the noise level.

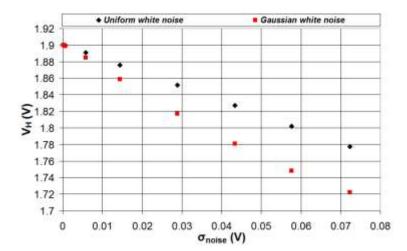


Figure 11. Average value of the detected Schmitt trigger threshold VH as function of the noise level.

Based on the obtained results, in order to achieve an acceptable measurement accuracy, it is of paramount importance to design a strategy to mitigate the impact of the noise for resistive sensor measurements carried out by the SMDI technique. A possible solution is to replace the standard SMDI measurement setup presented in Figure 1 with the proposed measurement setup shown in Figure 12.

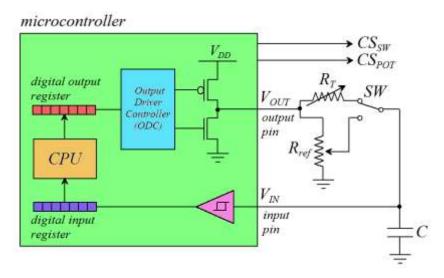


Figure 12. Proposed measurement setup to mitigate the impact of noise in SMDI-based measurements of resistive sensors.

The resistor R_T in Figure 12, represents the resistive sensor under test, while R_{ref} is a digital potentiometer, and SW is an analog switch controlled by the microcontroller digital output pins (CS_{SW} and CS_{POT}). In normal operating conditions, the switch SW connects the resistive sensor R_T between the microcontroller input and output pins, and the sensor value is measured using the procedure described in Section 2.1. The sensor value (R_T) is calculated by comparing the measured period (T_P) with a set of reference values determined during a calibration procedure and stored in a look-up table.

The calibration procedure is carried out at regular time intervals to guarantee that all the measurements carried out after the calibration experience the same noise level as during the calibration.

During the calibration procedure, the switch SW is set to disconnect the sensor R_T and connect the digital potentiometer R_{ref} between the microcontroller input and output pins. At this point, the period T_P defined in Equation (2) is measured (by averaging on an adequate number of measurements) for each value of the digital potentiometer R_{ref} (between 0Ω and the digital potentiometer full scale resistance with step ΔR_{ref}). The look-up table presenting the measured resistance value for the different values of the measured period is stored in the microcontroller memory. During the normal operating conditions, this look-up table is used to estimate the sensor resistance from the measured period T_P . More accurate estimation of the sensor resistance R_T is possible by using a digital potentiometer with higher resolution (i.e., lower ΔR_{ref}) but at the cost of higher memory occupation to store the calibration look-up table.

Simulations have been carried out to evaluate the maximum error in R_T estimation as function of the digital potentiometer resolution. The simulations have been performed for the case of uniform white noise with 40 mV peak-to-peak voltage. Both the sensor R_T (during the normal operating conditions) and the potentiometer R_{ref} (during the calibration procedure) are determined by averaging 50 measurements. A dataset of 50 different R_T

values was generated with a uniform probability distribution between 100 Ω and 10 k Ω . The simulation results have shown that the sensor relative error (Δ error as defined in Equation (7)) decreases, as expected, by increasing the digital potentiometer resolution (i.e., decreasing Δ Rref): we obtained a value of Δ error of 2.49% for Δ Rref = 25 Ω , a value of Δ error of 2.02% for Δ Rref = 20 Ω , a value of Δ error of 1.25% for Δ Rref = 12.5 Ω , a value of Δ error of 0.85% for Δ Rref = 10 Ω , and a value of Δ error of 0.46% for Δ Rref = 3.33 Ω . Decreasing Δ Rref below 3.33 Ω does not produce any improvement in the measurement accuracy.

5. Experimental Measurements on a Microcontroller

In this section, the simulation results presented in the previous sections are validated by experimental measurements carried out on a low-cost microcontroller.

The measurement setup is shown in Figure 13. It consists of a Nucleo-L152RE development board that integrates a STM32L152RET6 microcontroller (ST Microelectronics, Geneva, Switzerland) and a laptop PC that is used to communicate with the microcontroller board using the USB-UART interface.

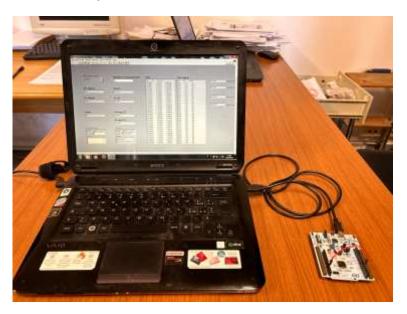


Figure 13. Experimental measurement setup to estimate the Schmitt trigger threshold voltages under different noise levels.

The threshold voltages (V_H and V_L) of the Schmitt trigger integrated in an input pin of the microcontroller have been measured (using the integrated 12-bit DAC) under different noise levels, with the following procedure.

- 1. The output of the microcontroller DAC is shorted with the input pin to be tested using a cable. Cables of three different lengths (11.5 cm, 26.5 cm, and 102.5 cm) were tested, since the longer the cable, the higher the probability that electromagnetic interference degrades the signal-to-noise ratio.
- 2. Meanwhile, the microcontroller generates an analog voltage at the DAC output that increases from 0 V to 3.3 V, with steps of 12.89 mV. After the DAC output voltage is increased to e new value, the microcontroller waits 2 ms to allow the voltage stabilization, and then reads the value of the digital input pin. The Schmitt trigger threshold V_H is estimated as the DAC output voltage for which the input pin logic value switches from 0 to 1.
- 3. Then, the microcontroller generates an analog voltage at the DAC output that decreases from 3.3 V to 0 V, with steps of 12.89 mV. Again, after the DAC output voltage is decreased to a new value, the microcontroller waits 2 ms to allow the voltage

stabilization, and then reads the value of the digital input pin. The Schmitt trigger threshold $V_{\rm L}$ is estimated as the DAC output voltage for which the input pin logic value switches from 1 to 0.

The values of V_H and V_L have been measured 100 times and the measured values (average value and standard deviation) are reported in Figure 14 (for V_H) and in Figure 15 (for V_L) as function of the cable length.

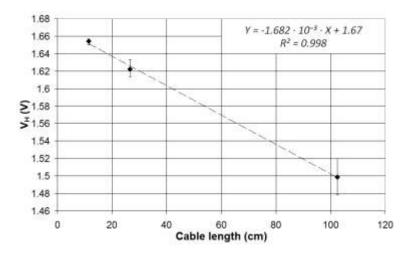


Figure 14. Measured values of the Schmitt trigger threshold voltage V_H as function of the cable length.

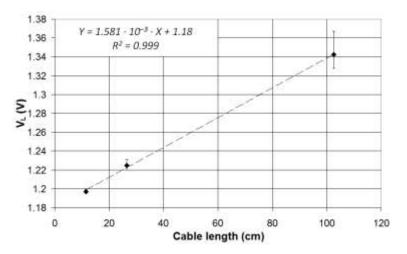


Figure 15. Measured values of the Schmitt trigger threshold voltage V_L as function of the cable length.

As can be seen, the measured Schmitt trigger threshold voltage V_H (V_L) decreases (increases) linearly (high determination coefficient, $R^2 > 0.998$) with the cable length, and thus with the electrical noise level. These experimental measurements confirm the simulation results, where the estimated Schmitt trigger threshold voltages change linearly with the standard deviation of the white noise signal (Figures 10 and 11).

6. Conclusions

In this paper, it was evaluated the impact of noise on the accuracy of resistive sensor measurements carried out by the sensor-to-microcontroller direct interface (SMDI) technique. The study was carried out by electrical level simulations with the software LTSpice and preliminary experimental measurements. The measurement accuracy of a resistive sensor was evaluated under different levels of white noise both in the case of SMDI-based

measurements and ADC-based measurements. The simulation results have shown that, while in the case of ADC-based measurements the impact of the noise can be mitigated by averaging on a large number of measurements, in the case of SMDI-based measurements this solution is ineffective. Thus, a mitigation strategy was proposed to allow accurate measurements of resistive sensors using the SMDI technique in a noisy environment.

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