

Optimization of the Geometry of a MEMS Testing Device for SiO₂—Polysilicon Interface Characterization [†]

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Abstract: Microelectromechanical systems (MEMS) are small-scale devices that combine mechanical and electrical components made through micro-fabrication techniques. These devices have revolutionized numerous technological applications, owing to their miniaturization and versatile functionalities. However, the reliability of MEMS devices remains of critical concern, especially when operating in harsh conditions like high temperature and humidity. The unknown behavior of the structural parts under cyclic loading conditions, possibly affected by microfabrication defects, poses in fact challenges in ensuring their long-term performance. This research focuses on addressing the reliability problem by investigating the fatigue-induced delamination in polysilicon-based MEMS structures, specifically at the interface between SiO₂ and polysilicon. Dedicated test structures with piezoelectric actuation and sensing for close loop operation have been designed, aiming to maximize the stress in regions susceptible to delamination. By carefully designing the structures, a localized stress concentration is induced to facilitate the said delamination and help understanding the underlying failure mechanism. The optimization has been performed by taking advantage of finite element analyses, allowing a comprehensive analysis of the mechanical response of the movable parts of the polysilicon MEMS under cyclic loading.

Keywords: MEMS; reliability; fatigue and fracture; geometry optimization

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1. Introduction

In the realm of microelectromechanical systems (MEMS), the fusion of miniaturized mechanical and electrical components through micro-fabrication techniques has spearheaded a technological revolution. These devices have demonstrated immense potential for multiple applications, driven by their compact form and multifaceted functionalities [1–3]. However, amid the proliferation of their application, the crucial concern of reliability looms large, particularly when they are exposed to challenging operational conditions such as high load cycles, elevated temperatures and high humidity [4]. A thorough assessment of mechanical reliability stands as a crucial requirement to propel the ongoing development of MEMS.

The dominance of polycrystalline silicon in crafting MEMS designed to sustain high-frequency oscillations, is driven by its exceptional mechanical properties, outperforming many alternative materials. Despite its inherent brittleness and the absence of dislocation motion at temperatures below 900 °C, operating conditions do not typically induce fatigue mechanisms [1,4]. Nevertheless, in some cases the MEMS devices exhibit heightened susceptibility to environmental factors that impact both the mechanical and electrical characteristics of the device. Paradoxically, fatigue stands as one of the critical failure

mechanisms in such systems, highlighting the importance of thorough consideration and analysis of the mechanical properties of polysilicon [5], see also [6,7].

An additional failure mode, likely to manifest under cyclic loading due to elevated interfacial stress levels and often in conjunction with fatigue, is delamination. This localized cracking-like failure mode typically occurs at the interface between silicon dioxide and polycrystalline silicon. Fatigue and delamination phenomena both lead to a progressive shift in resonance frequency, structural stiffness (also affected by uncertainties at the microscale, see [8–12]), and electrical resistance, thereby affecting the long-term reliability of the devices [5,13].

Effectively replicating fatigue failures through experimental tests, needs the utilization of setups enabling relatively high-frequency testing, ideally in the range of kHz. Such high frequency driving enables a significant number of cycles to be achieved within a reasonable timeframe, critical for accurate fatigue analysis. In this context, on-chip tests emerge as the optimal choice, given their capability to operate at large frequency ranges [14].

By employing purpose-built test structures leveraging piezoelectric actuation and sensing for closed-loop operation, this research seeks to optimize the stress field within the movable structure of ad-hoc designed MEMS test structures. Advantage is taken of finite element analyses, to enhance the stress concentrations at the interface between SiO₂ and polysilicon, in order to possibly induce fatigue-driven delamination. The optimization procedure is based on static analyses, to get an idea of the best shape of the mechanical parts to induce a large stress concentration at the said interface, and so heading toward failure analysis in the experimental campaign to follow.

2. Materials and Methods

As anticipated, a polysilicon-based MEMS test structure has been ad-hoc designed to maximize the stress concentration, possibly leading to delamination-driven failure modes. The optimization of the geometry of the structure has been made possible by the finite element software COMSOL Multiphysics®, through its MEMS module [15].

Figure 1 displays the initial geometry used for the optimization process, which is a bridge-like test structure actuated in bending mode. The main material layers are single crystal silicon and silicon dioxide, on top of which the polycrystalline silicon movable structure is laid. In addition, four PZT-based patches are used for actuation and sensing, as highlighted in red in the figure. The shape optimization procedure has been carried out by varying the length of the beam connecting the plate and the SiO₂-polycrystalline silicon interface, and by also allowing for the following constraints:

- The failure-governing principal stress component at the interface should be as high as possible, ideally close to 1 GPa in order to speed up the fatigue tests.
- The stress field in the polysilicon layer should not exceed the one at the interface, to avoid inducing brittle cracking in the latter region [16,17].

The numerical investigation includes two types of analysis, see Figure 2: (i) a stationary analysis, performed on the entire structure and characterized by a coarse mesh optimized to achieve a trade-off between accuracy and computational cost requirements; (ii) a stationary analysis performed only on the region under study, namely on the central portion of the device, characterized by a finer mesh optimized to achieve accuracy in terms of the stress field. To take into account the non-linear geometric effects linked to the deformed configuration of the structure, a sufficiently high driving voltage is adopted in the first stationary analysis. Afterward, this solution is used as the starting configuration for the evaluation of the stress field using the finer mesh of the second analysis.

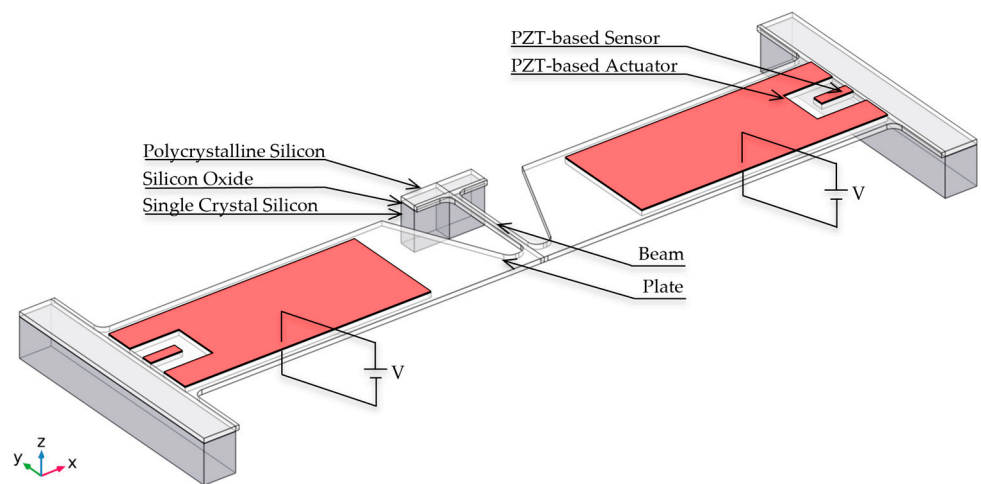


Figure 1. Sketch of the polysilicon-based MEMS test structure.

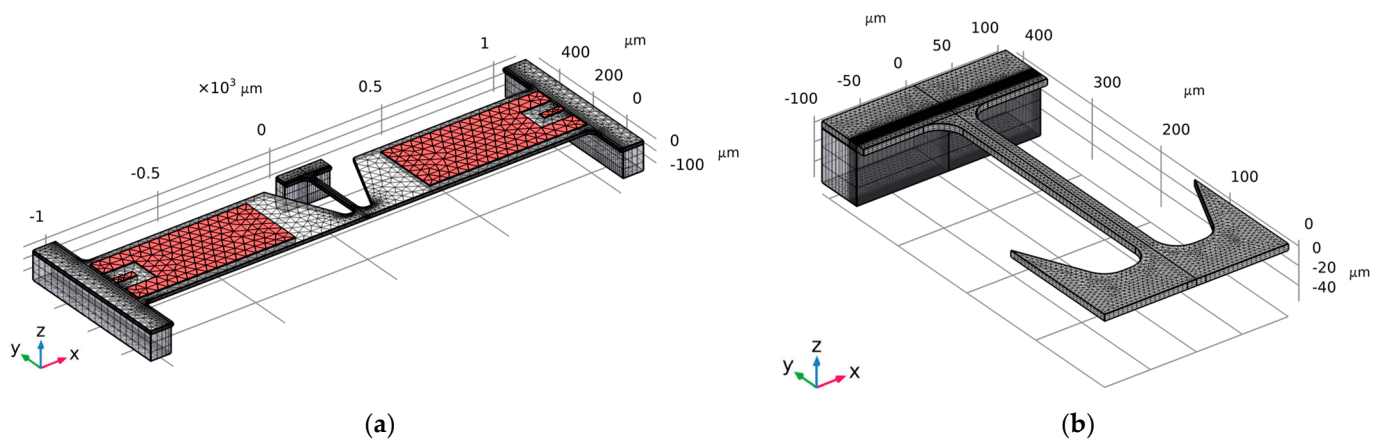


Figure 2. (a) Coarse mesh of the entire structure; (b) refined mesh on a smaller part of the structure.

The mechanical properties of the main materials that make up the test structure are gathered in Table 1. The properties of single crystal silicon in the <100> crystallographic direction are shown [18].

Table 1. Electro-mechanical properties of the main materials of the test structure.

| Material | Thickness (μm) | Mass Density (kg/m ³) | Young's Modulus (GPa) | Poisson's Ratio | Shear Modulus (GPa) |
|-------------------------|----------------|-----------------------------------|-----------------------|-----------------|---------------------|
| Single Crystal Silicon | 110 | 2330 | 130 | 0.278 | 79.6 |
| Silicon Oxide | 1 | 2200 | 70 | 0.17 | 29.9 |
| Polycrystalline Silicon | 13 | 2320 | 160 | 0.22 | 65.6 |
| PZT | 2 | 7600 | 70 | 0.33 | 26.3 |

3. Results and Discussion

A series of stationary analyses has been conducted for a beam length L ranging between 60 μm and 300 μm, at a given applied driving voltage. Taking advantage of the 3-1 piezoelectric coupling mechanism of the PZT, an axial deformation of the beam is induced, leading to a bending-dominated structural deformation mode and, therefore, to displacements in the out-of-plane, z -direction. Being the structure fully constrained at its two side ends, the load is transmitted through the beam and measured through the

deflection at the center, to also quantify the induced solution at the interface where the stress must be intensified.

The results of the local analyses featuring the finest mesh, are depicted in Figure 3. It is shown that, as expected, for very high values of L the structure gets very compliant, as highlighted by the large deflection values attained, and the stress transmitted to the SiO₂–polysilicon interface becomes very small if compared to the stress exhibited in the polysilicon layer, specifically along the rounded corners highlighted in red in the figure. As the beam length L decreases, the vertical displacement at the middle point decreases as well, due to a stiffening of the structure, and so the principal stress in the rounded region. On the contrary, the stress at the SiO₂–polysilicon interface keeps increasing until it reaches a maximum value of about 800 MPa, notably higher than the stress in the polysilicon amounting to about 700 MPa. Afterward, the stress at the interface starts to drop due to the constrain joint at the center of the movable plate, which induces a stress redistribution throughout the entire structure.

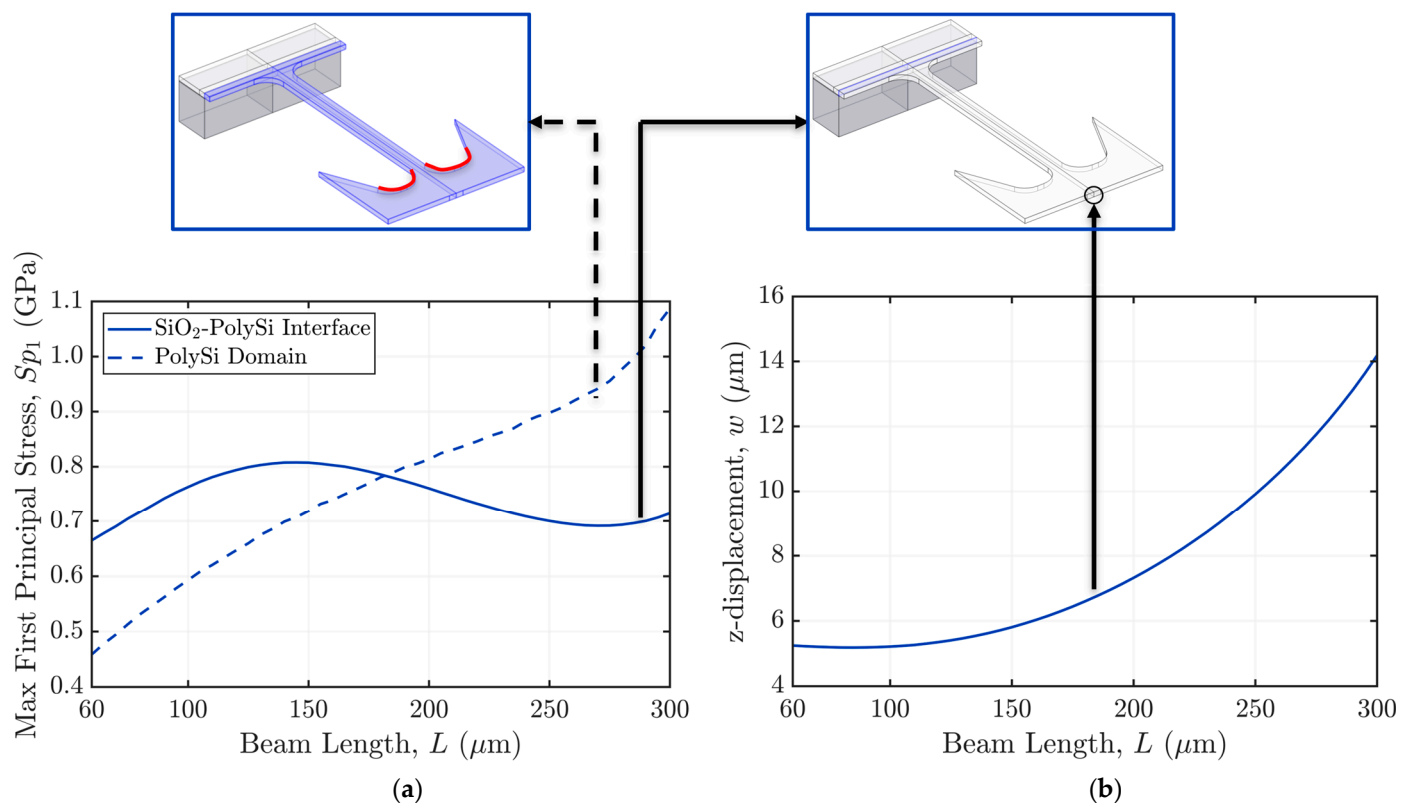


Figure 3. Effects of the beam length L : (a) on the maximum stress in the polycrystalline silicon domain (dashed line) and on the SiO₂-polycrystalline silicon interface (continuous line); (b) on the out-of-plane displacement, or deflection at the central point highlighted with the black circle.

4. Conclusions

In this study, a new design for a MEMS test structure based on piezoelectric actuation and sensing for close-loop operation has been reported. By means of finite element analyses, the geometry has been optimized in order to maximize the stress at the SiO₂-polysilicon interface, as a concentration can lead in real-life situations to delamination events. A maximum stress equivalent to about 800 MPa has been attained in the solution, which resulted to be higher than the stress in the polysilicon, to increase the probability of a localized crack-driven failure at such interface, and close to the target value of 1 GPa needed to speed up the fatigue tests.

The findings will be further developed by taking into consideration other geometric parameters in the optimization process, that may lead to a higher stress intensification at

the interface. The actual dynamic response of the structures to sinusoidal time-varying electric potential will be also assessed, and compared to the outcome of an experimental campaign.

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Conflicts of Interest: The authors declare no conflict of interest.

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