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## Assessment of polysilicon film properties through on-chip tests

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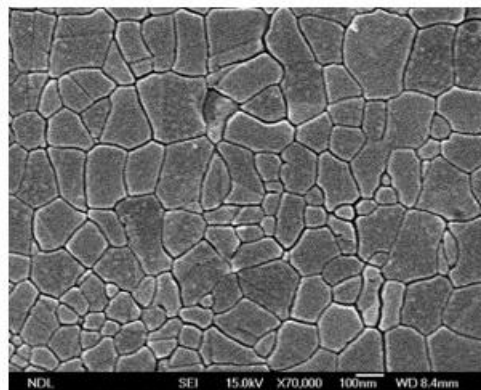
- ❖ Silicon the most common material used in Microelectromechanical Systems(MEMS)
- ❖ Anisotropic crystalline material whose material properties depends on orientation relative to the crystal lattice
- ❖ Characteristic length of mechanical components can be compared to the size of grains

- ❖ **Morphology** & **crystal lattice orientation** are not known

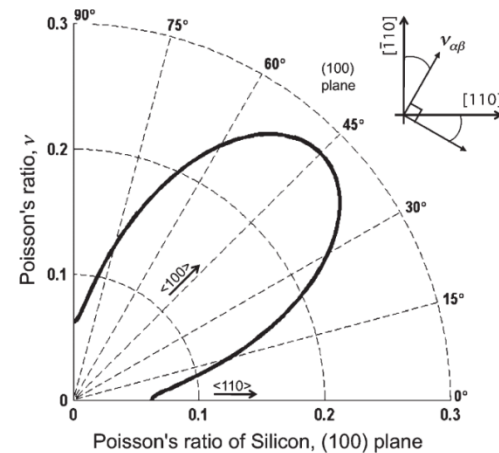
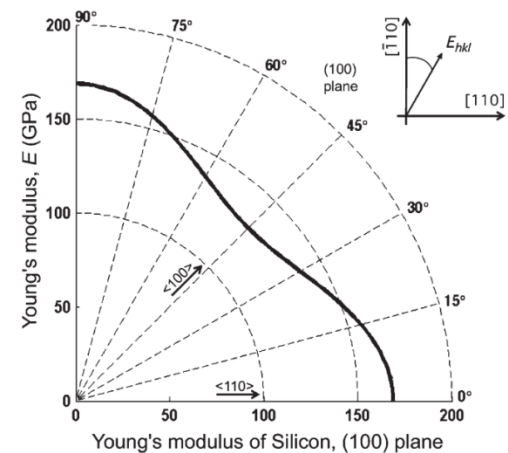
**Sources of uncertainties in mechanical response**

❖ These sources of uncertainties should to be addressed

- ❖ Experimentally
- ❖ Analytical and numerical modeling



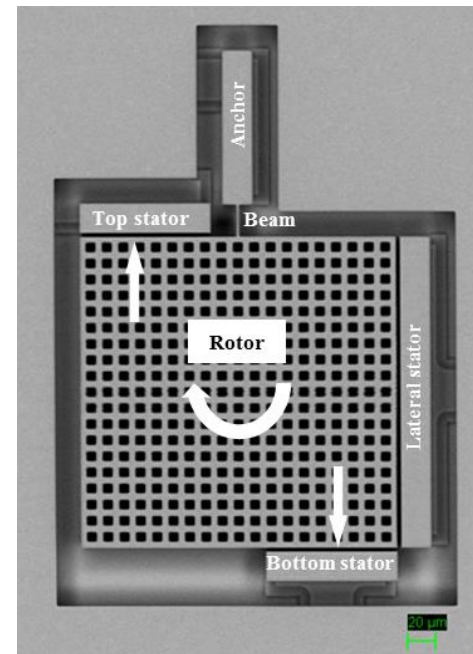
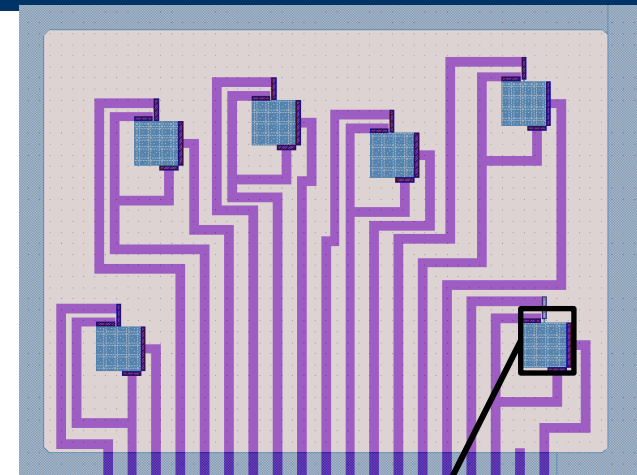
<http://www.ieo.nctu.edu.tw/leo/htms/photon/Laser%20Annealing.htm>



Hopcroft, M.A., et al., "What is the Young Modulus of Silicon?", *JMM*, 2010



- ❖ An on-chip test adopted
- ❖ The specimen is a micro-beam made of polysilicon with average grain size of 500nm
- ❖ 6 devices featuring
  - ❖ Width:  $2\mu\text{m}$
  - ❖ Length: 2, 3, 4, 5, 10, 20  $\mu\text{m}$
- ❖ Electrostatic actuation/sensing
- ❖ Two sets of conductors providing 4 combinations of sensing /actuation
- ❖ The electromechanical response varies between devices either due to
  - ❖ Geometrical uncertainties
  - ❖ Material uncertainties

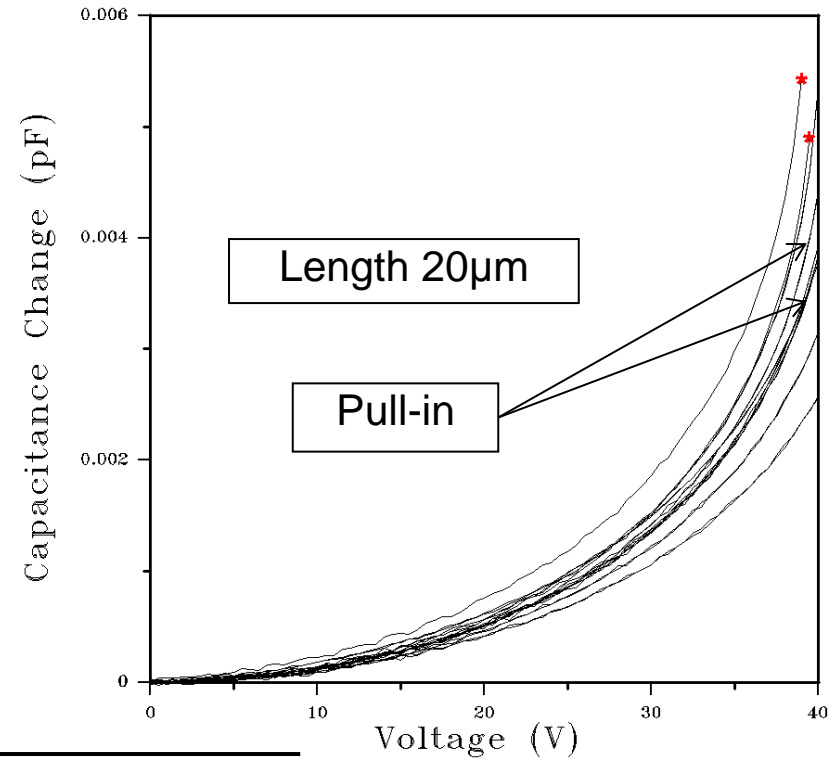




# Experimental tests rotational mode

- The measurement is repetitive and reproducible except for the ones with pull-in
- The bias voltage difference is increased and then decreased to zero (max40V)

•Pull-in at 39.25-39.75



Parameter	value	Parameter	value
beam length ( $l$ )	2, 3, 4, 5, 10, 20 $\mu\text{m}$	referenced initial gap between rotor and stators ( $g_o$ )	2 $\mu\text{m}$
beam thickness ( $h$ )	2 $\mu\text{m}$	$a$	17 $\mu\text{m}$
out-of-plane thickness ( $w$ )	22 $\mu\text{m}$	$b$	100 $\mu\text{m}$



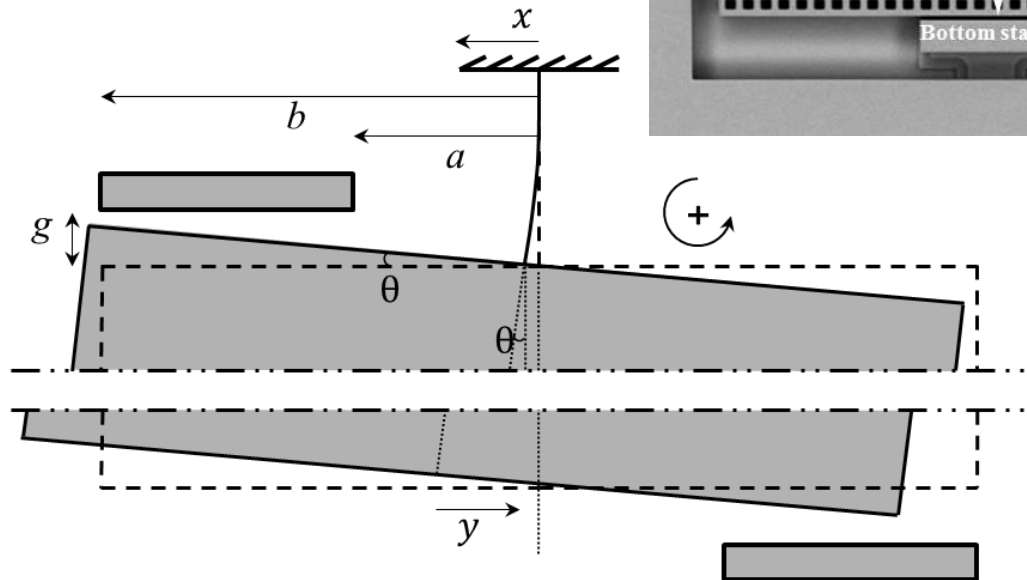
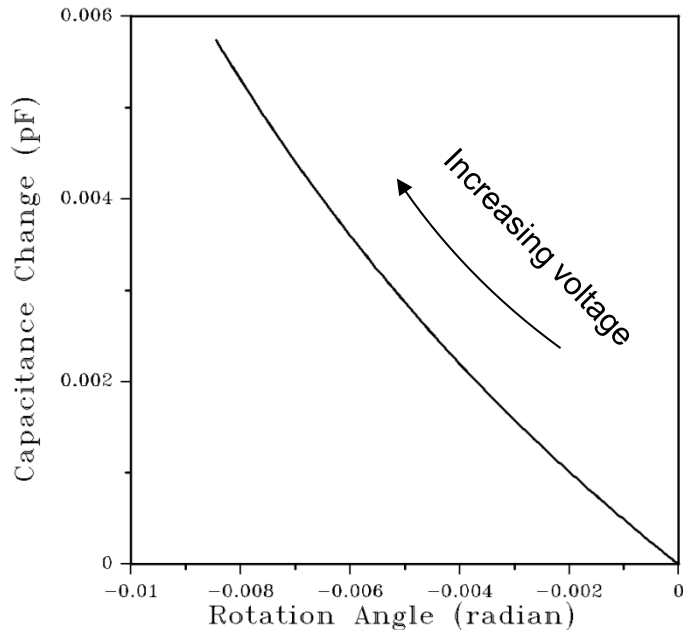
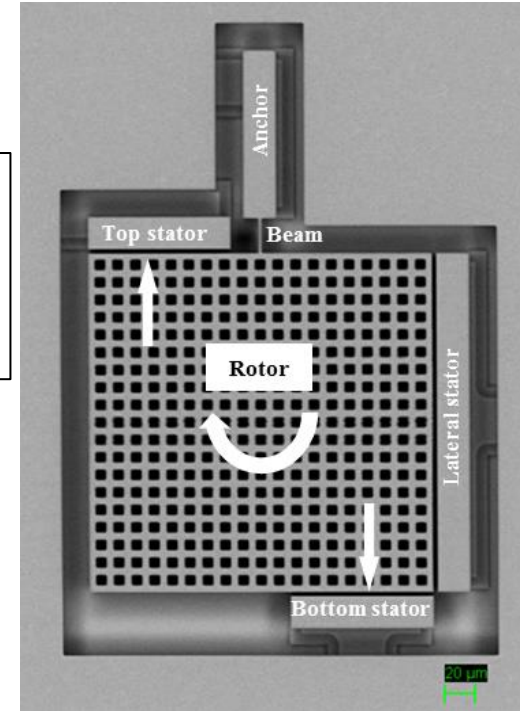
## ❖ Simplifications:

- ❖ Big mass to be rigid
- ❖ The micro-beam kinematics to be governed by Euler-Bernoulli
- ❖ Electric fringe field neglected
- ❖ No deformation at the anchor

Closed form solution for Capacitance change as a function of voltage difference

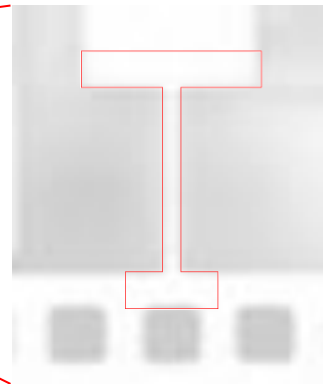
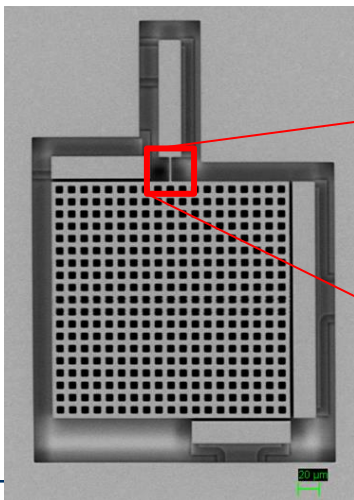
Coulomb's law for parallel charged plates

$$F_{elec} = \frac{1}{2} \frac{\epsilon_0 AV^2}{gap^2}$$





- ❖ Simplifications:
  - ❖ Electric fringe field neglected
- ❖ Parametric geometry
  - ❖ Parametric study on overetch values
- ❖ Big mass is modelled by homogenized isotropic elastic properties
- ❖ Two scenarios for beam modeling
  - ❖ Homogeneous model
    - Bounds of response
  - ❖ Heterogeneous model



For each given  $V$



$$KU = F_{ext} + F_{elec}(V) \rightarrow \text{Structural domain}$$
$$K_{dielec}(U)V = Q(U, V) \rightarrow \text{Electrostatic domain}$$

- ❖ Nonlinear coupled field analysis
    - ❖ Electrostatic forces on boundary nodes
    - ❖ Deformation effect the dielectric and electric field
- $U$  is used to update the geometry
- ❖ Electrostatic analysis for calculation of mutual capacitance between conductor systems





# Numerical Modeling Random Morphology

- ❖ Two scenarios for beam modeling
  - ❖ Homogeneous model
    - Bounds of response
  - ❖ Heterogeneous model
    - Monte Carlo simulation



❖ Voronoi diagram

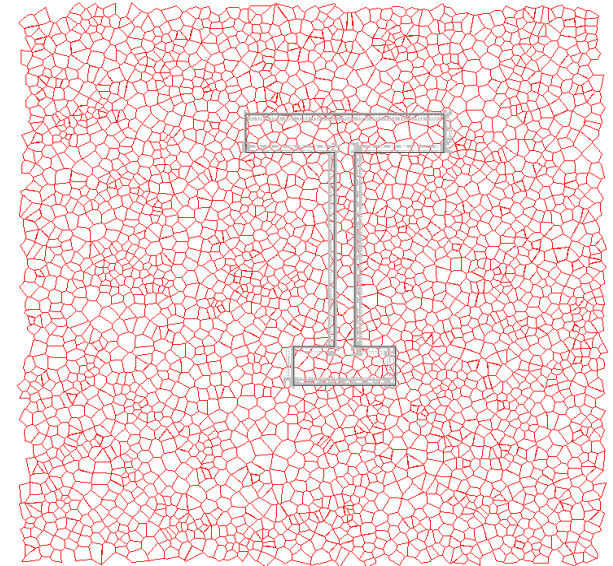
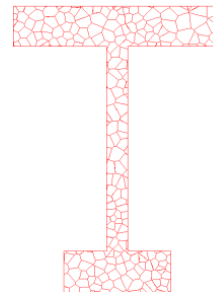
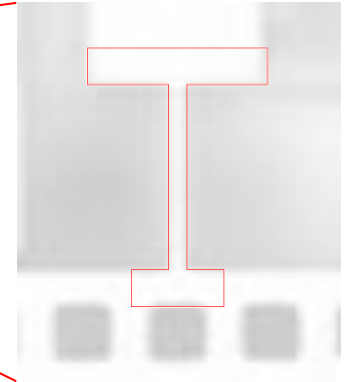
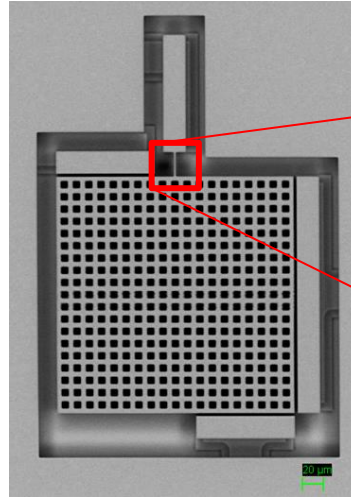


❖ 100 times

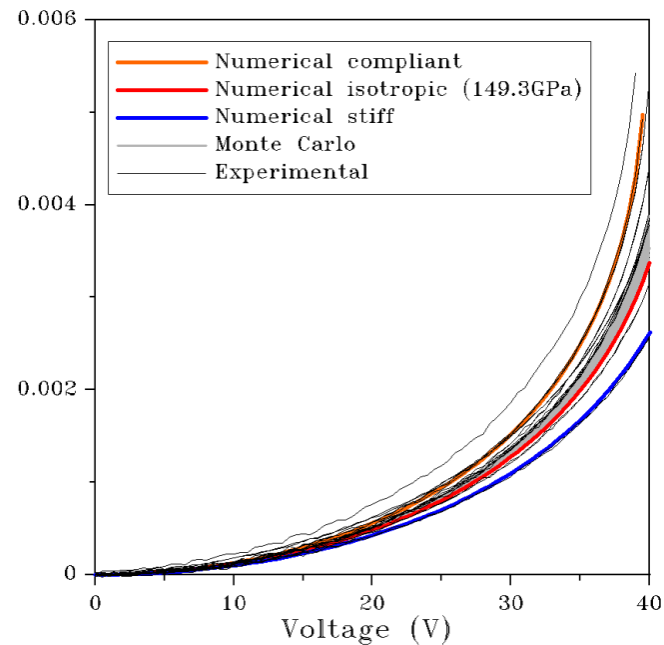
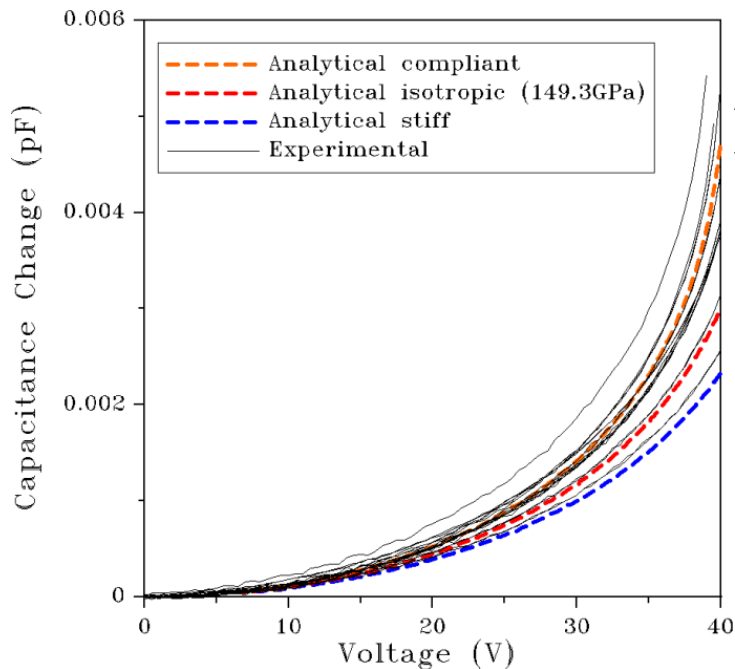
- ❖ Random mask position
- ❖ Random lattice orientation



❖ Random morphology at beam and its anchors



- ❖ Three different values for crystalline orientation of silicon
  - ❖ Direction  $\langle 110 \rangle$   $E=169\text{GPa}$  (Stiff)
  - ❖ Direction  $\langle 100 \rangle$   $E=130\text{GPa}$  (Compliant)
  - ❖ Homogenized value  $E=149.3\text{ GPa}^*$
- ❖ Good bounds are provided for the experimental data
- ❖ Overetch can happen
  - ❖ Intensity depends on the geometry
- ❖ Geometry can vary slightly from device to device
- ❖ Overetch effect needs to be considered



\*Mariani, S., et.al., Overall elastic properties of polysilicon films: a statistical investigation of the effects of polycrystal morphology. *Int J Multiscale Com*,2011.





- ❖ Sources of material uncertainties in polysilicon film morphology is studied
- ❖ An on-chip test is designed to study the effect of morphology on the response of a micro beam
- ❖ The experimental results are modelled analytically and numerically
- ❖ Both models can bound the response scatterings
- ❖ The Monte Carlo simulation is carried out
- ❖ The effects of overetch at the response scattering should be studied

## Acknowledgment

- ❖ Financial support provided by STMicroelectronics through project MaRe (MAterail RELiability) is gratefully acknowledged.





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**Thank you for your  
kind attention!**